

General Description

SFGMOS[®]

DS(ON),

low gate charge, fast switching and excellent avalanche characteristics. The high V_{th} series is specially optimized for high systems with gate driving voltage greater than 10V.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Fast switching and soft recovery



Applications

- Switched mode power supply
- Motor driver
- Battery protection
- DC-DC convertor
- Solar inverter
- UPS and energy inverter

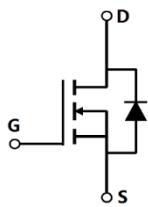
Key Performance Parameters

Parameter	Value	Unit
$V_{DS, min} @ T_{j(max)}$	100	V
$I_D, pulse$	450	A
$R_{DS(ON), max} @ V_{GS}=10V$	3.5	
Q_g	87.8	nC

Marking Information

Product Name	Package	Marking
SFG150N10KF	TO263	SFG150N10K

Package & Pin information



Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	V_{DS}	100	V
Gate source voltage	V_{GS}	± 20	V
Continuous drain current ¹⁾ , $T_C=25^\circ\text{C}$	I_D	150	A
Pulsed drain current ²⁾ , $T_C=25^\circ\text{C}$	$I_{D,\text{pulse}}$	450	A
Continuous diode forward current ¹⁾ , $T_C=25^\circ\text{C}$	I_S	150	A
Diode pulsed current ²⁾ , $T_C=25^\circ\text{C}$	$I_{S,\text{pulse}}$	450	A
Power dissipation ³⁾ , $T_C=25^\circ\text{C}$	P_D	250	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	265	mJ
Operation and storage temperature	$T_{stg} \quad T_j$	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	R	0.5	$^\circ\text{C}/\text{W}$
Thermal resistance, junction-ambient ⁴⁾	R	62	$^\circ\text{C}/\text{W}$

Electrical Characteristics at $T_j=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	100			V	$V_{GS}=0 \text{ V}, I_D=250 \text{ A}$
Gate threshold voltage	$V_{GS(\text{th})}$	2.0		4.0	V	$V_{DS}=V_{GS}, I_D=250 \text{ A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		3.22	3.50		$V_{GS}=10 \text{ V}, I_D=30 \text{ A}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=20 \text{ V}$
				-100		$V_{GS}=-20 \text{ V}$
Drain-source leakage current	I_{DSS}			1	A	$V_{DS}=100 \text{ V}, V_{GS}=0 \text{ V}$
Gate resistance	R_G		4.9			

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C _{iss}		6850		pF	V _{GS} =0 V, V _{DS} =25 V, 100 kHz
Output capacitance	C _{oss}		3170		pF	
Reverse transfer capacitance	C _{rss}		251		pF	
Turn-on delay time	t _{d(on)}		32		ns	V _{GS} =10 V, V _{DS} =50 V, R _G I _D =65 A
Rise time	t _r		138		ns	
Turn-off delay time	t _{d(off)}		88		ns	
Fall time	t _f		106		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q _g		87.8		nC	V _{GS} =10 V, V _{DS} =50 V, I _D =65 A
Gate-source charge	Q _{gs}		27.1		nC	
Gate-drain charge	Q _{gd}		22.9		nC	
Gate plateau voltage	V _{plateau}		5.5		V	

Electrical Characteristics Diagrams

Test circuits and waveforms

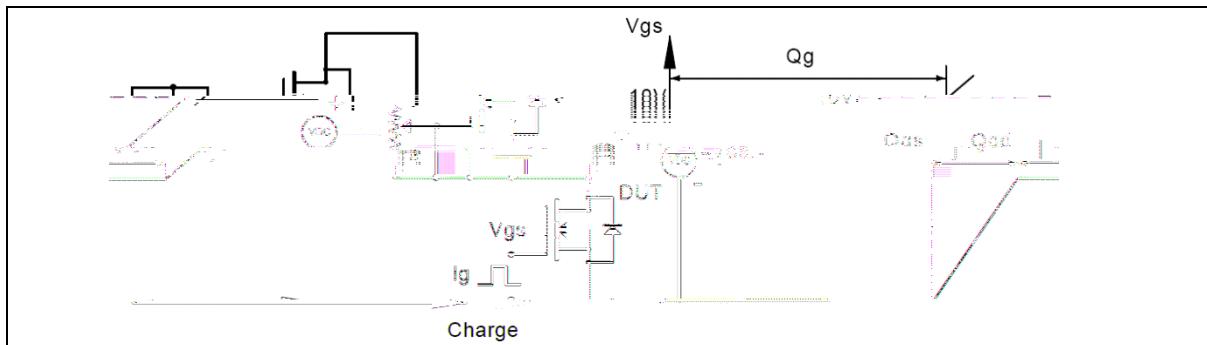


Figure 1. Gate charge test circuit & waveform

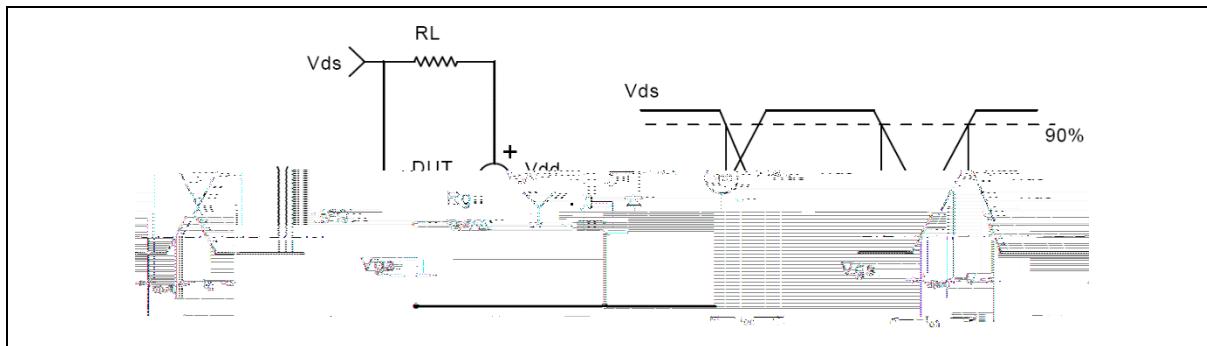


Figure 2. Switching time test circuit & waveform

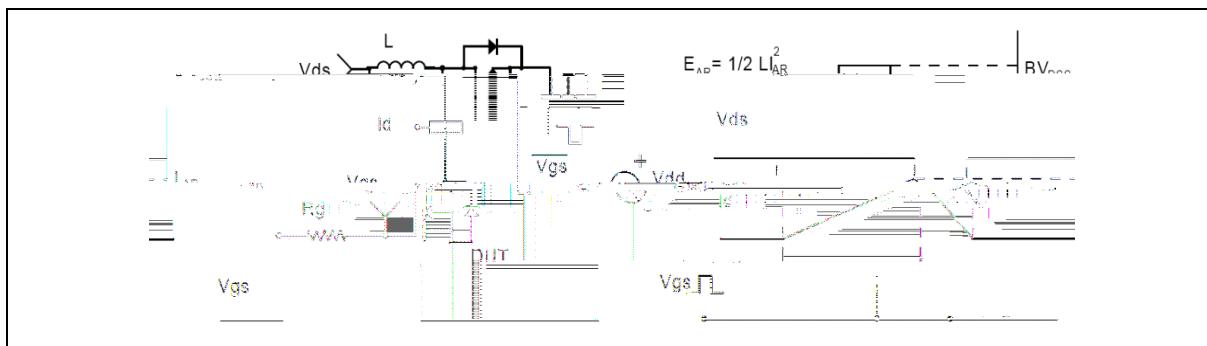


Figure 3. Unclamped inductive switching (UIS) test circuit & waveform

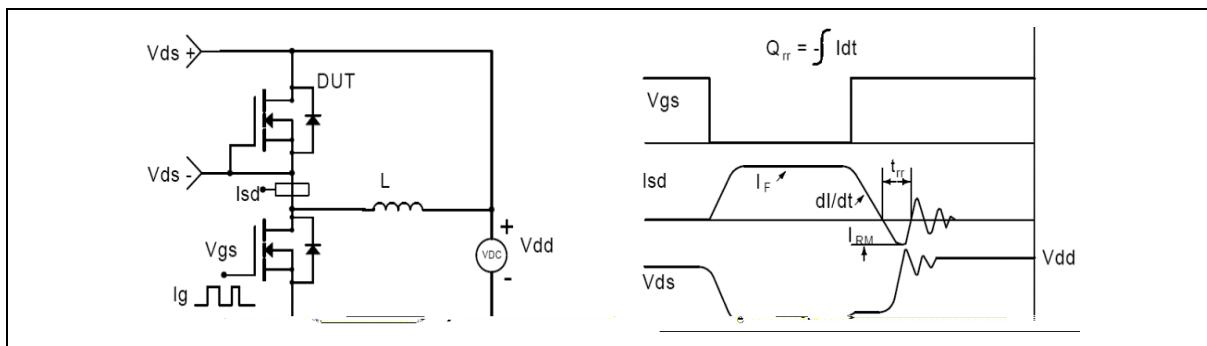


Figure 4. Diode reverse recovery test circuit & waveform

SFG150N10KF

Enhancement Mode N-

Package Information

Symbol	mm		
	Min	Nom	Max
A	4.40	4.50	4.60
A1	0.00	0.10	0.25
A2	2.20	2.40	2.60
b	0.76	-	0.89
b1	0.75	0.80	0.85
b2	1.23	-	1.37
b3	1.22	1.27	1.32
c	0.47	-	0.60
c1	0.46	0.51	0.56
c2	1.25	1.30	1.35
D	9.10	9.20	9.30
D1	8.00	-	-
E	9.80	9.90	10.00
E1	7.80	-	-
e	2.54 BSC		
H	14.90	15.30	15.70

Ordering Information

Package Type	Units/Reel	Reels / Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO263-C	800	1	800	5	4000
TO263-J	800	1	800	10	8000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFG150N10KF	TO263	yes	yes	yes

Legal Disclaimer

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