

General Description

FSMOS[®]

low

$R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. The low V_{th} series is specially designed to use in synchronous rectification power systems with low driving voltage.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent reliability and uniformity
- Fast switching and soft recovery



Applications

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC convertor
- Switched mode power supply

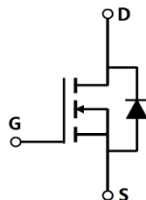
Key Performance Parameters

| Parameter | Value | Unit |
|-------------------------------|-------|------|
| $V_{DS, min} @ T_{j(max)}$ | 80 | V |
| $I_{D, pulse}$ | 192 | A |
| $R_{DS(ON) max} @ V_{GS}=10V$ | 7 | |
| Q_g | 28.9 | nC |

Marking Information

| Product Name | Package | Marking |
|--------------|---------|-----------|
| SFS08R07GF | PDFN5*6 | SFS08R07G |

Package & Pin information



Absolute Maximum Ratings at $T_j=25^{\circ}\text{C}$ unless otherwise noted

| Parameter | Symbol | Value | Unit |
|---|-----------------|------------|--------------------|
| Drain source voltage | V_{DS} | 80 | V |
| Gate source voltage | V_{GS} | ± 20 | V |
| Continuous drain current ¹⁾ , $T_C=25^{\circ}\text{C}$ | I_D | 64 | A |
| Pulsed drain current ²⁾ , $T_C=25^{\circ}\text{C}$ | $I_{D, pulse}$ | 192 | A |
| Continuous diode forward current ¹⁾ , $T_C=25^{\circ}\text{C}$ | I_S | 64 | A |
| Diode pulsed current ²⁾ , $T_C=25^{\circ}\text{C}$ | $I_{S, Pulse}$ | 192 | A |
| Power dissipation ³⁾ , $T_C=25^{\circ}\text{C}$ | P_D | 87 | W |
| Single pulsed avalanche energy ⁵⁾ | E_{AS} | 25 | mJ |
| Operation and storage temperature | T_{stg} T_j | -55 to 175 | $^{\circ}\text{C}$ |

Thermal Characteristics

| Parameter | Symbol | Value | Unit |
|--|--------|-------|----------------------|
| Thermal resistance, junction-case | R | 1.72 | $^{\circ}\text{C/W}$ |
| Thermal resistance, junction-ambient ⁴⁾ | R | 62 | $^{\circ}\text{C/W}$ |

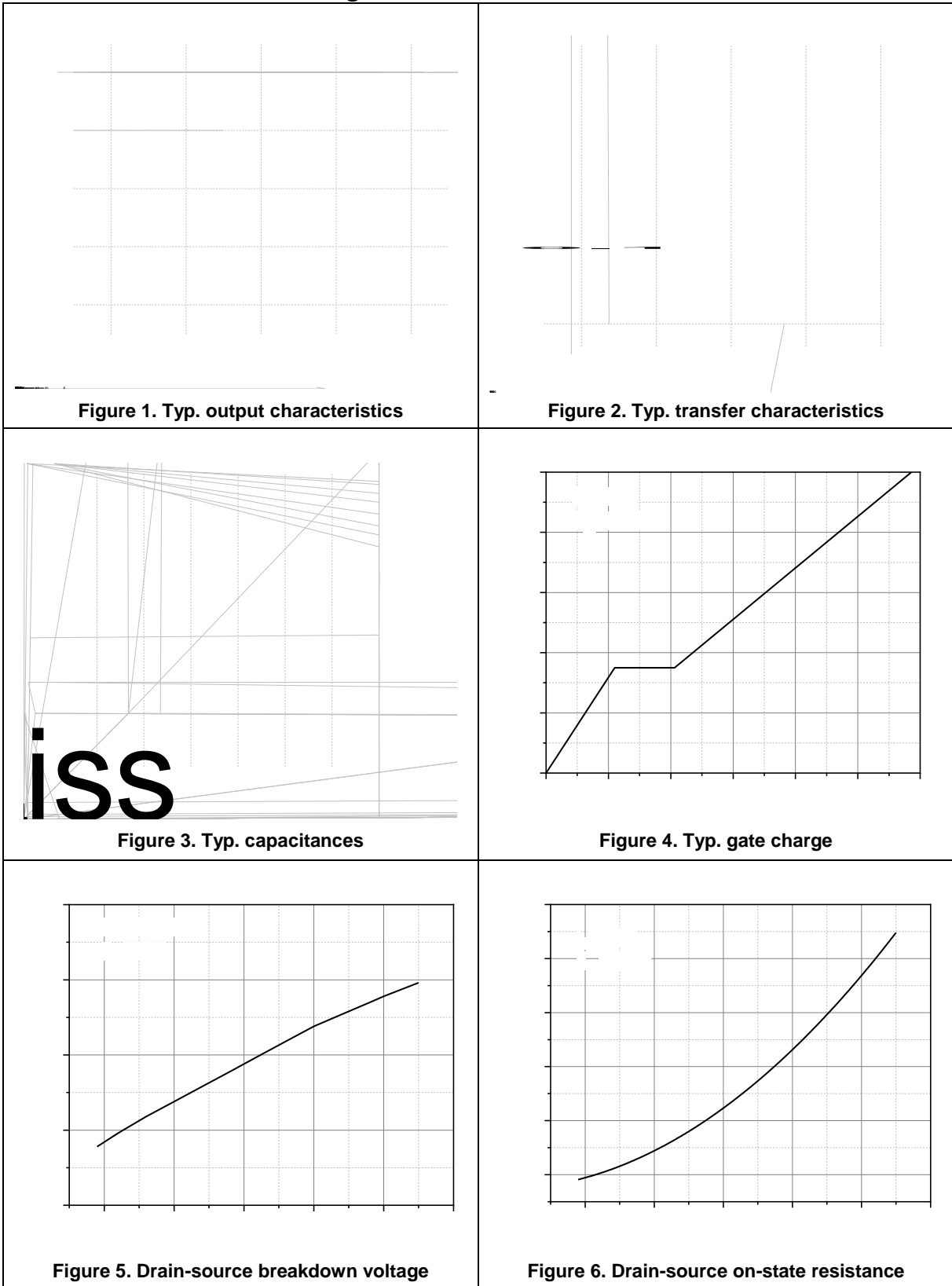
Electrical Characteristics at $T_j=25^{\circ}\text{C}$ unless otherwise specified

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Test condition |
|----------------------------------|--------------|------|------|------|------|--|
| Drain-source breakdown voltage | BV_{DSS} | 80 | | | V | $V_{GS}=0\text{ V}$, $I_D=250\text{ A}$ |
| Gate threshold voltage | $V_{GS(th)}$ | 1.0 | | 2.5 | V | $V_{DS}=V_{GS}$, $I_D=250\text{ A}$ |
| Drain-source on-state resistance | $R_{DS(ON)}$ | | 5.9 | 7 | | $V_{GS}=10\text{ V}$, $I_D=12\text{ A}$ |
| Drain-source on-state resistance | $R_{DS(ON)}$ | | 7.1 | 10 | | $V_{GS}=4.5\text{ V}$, $I_D=9\text{ A}$ |
| Gate-source leakage current | I_{GSS} | | | 100 | nA | $V_{GS}=20\text{ V}$ |
| | | | | -100 | | $V_{GS}=-20\text{ V}$ |
| Drain-source leakage current | I_{DSS} | | | 1 | A | $V_{DS}=80\text{ V}$, $V_{GS}=0\text{ V}$ |
| Gate resistance | R_G | | 3.3 | | | |

Dynamic Characteristics

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Test condition |
|------------------------------|--------------|------|------|------|------|--|
| Input capacitance | C_{iss} | | 2028 | | pF | $V_{GS}=0\text{ V}$, $V_{DS}=25\text{ V}$, kHz |
| Output capacitance | C_{oss} | | 717 | | pF | |
| Reverse transfer capacitance | C_{rss} | | 53.9 | | pF | |
| Turn-on delay time | $t_{d(on)}$ | | 22.2 | | ns | $V_{GS}=10\text{ V}$, $V_{DS}=50\text{ V}$, $R_G=2.5$ $I_D=25\text{ A}$ |
| Rise time | t_r | | 6.3 | | ns | |
| Turn-off delay time | $t_{d(off)}$ | | 47.5 | | ns | |
| Fall time | t_f | | 8.8 | | ns | |

Electrical Characteristics Diagrams



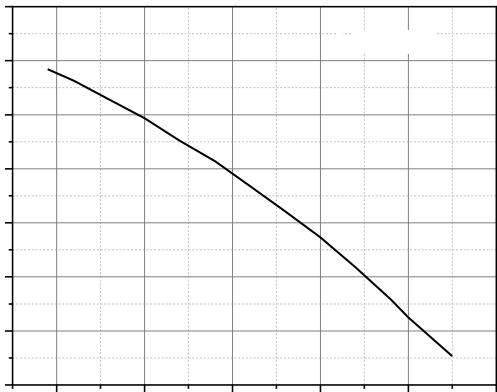


Figure 7. Threshold voltage

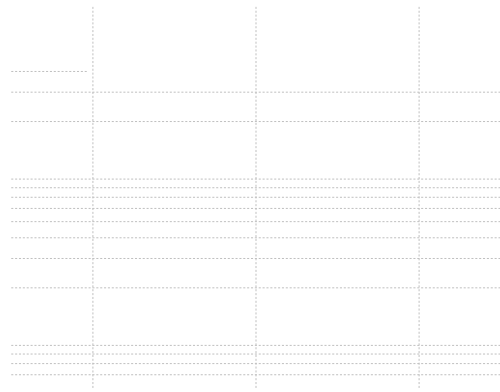


Figure 8. Forward characteristic of body diode

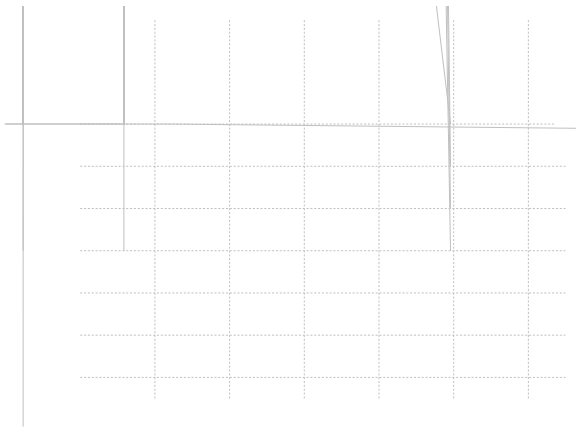


Figure 9. Drain-source on-state resistance

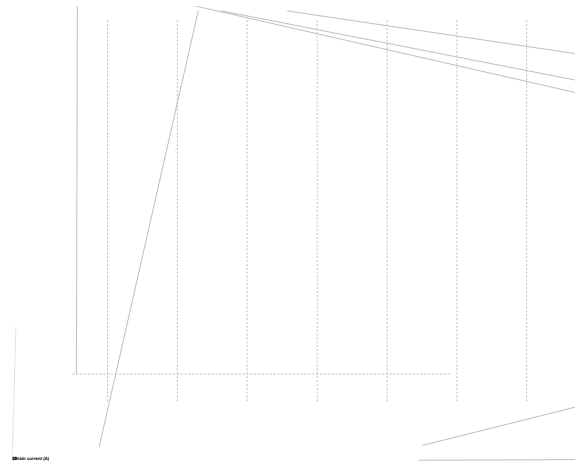


Figure 10. Drain current

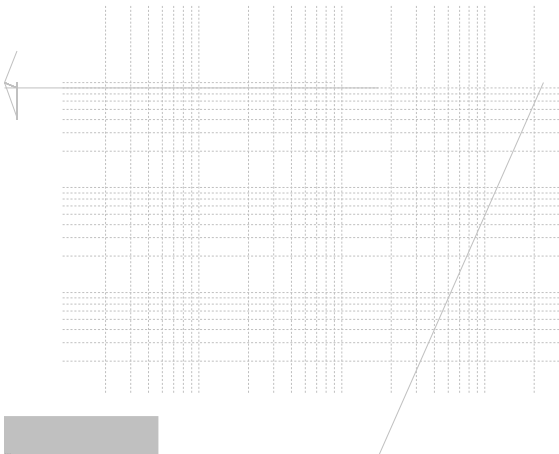


Figure 11. Safe operation area $T_c=25\text{ }^\circ\text{C}$

Figure 12. Max transient thermal impedance

Test circuits and waveforms

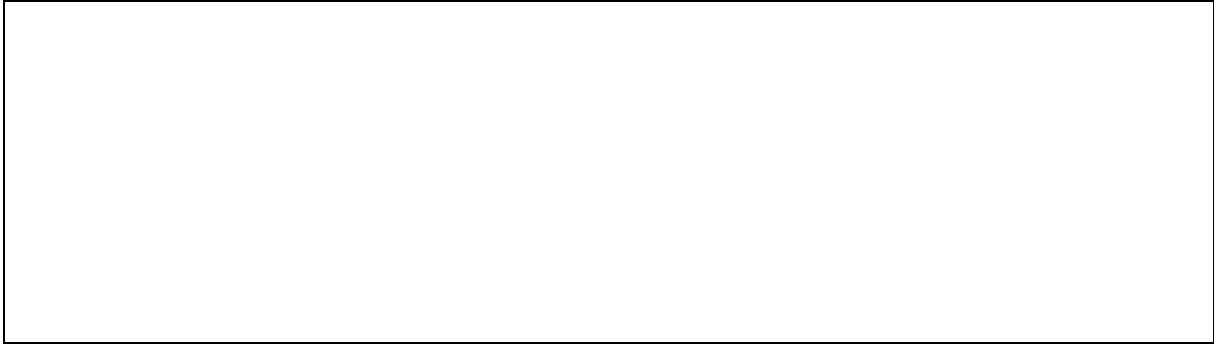


Figure 1. Gate charge test circuit & waveform

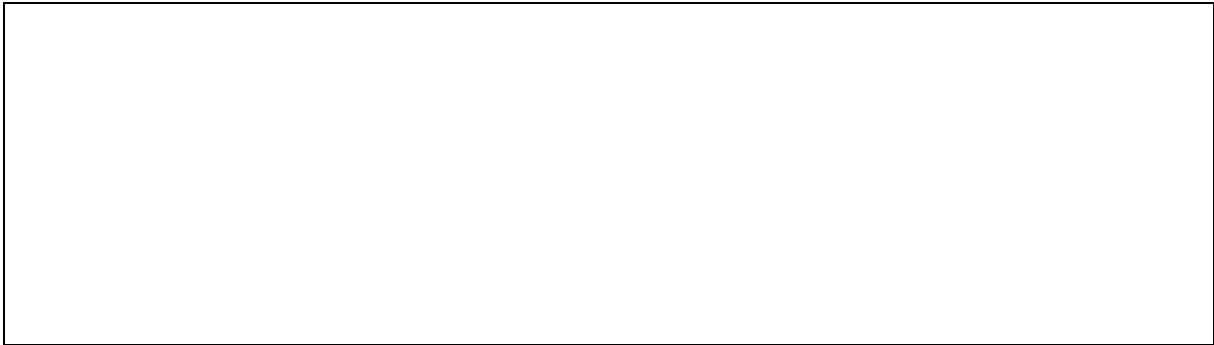


Figure 2. Switching time test circuit & waveforms

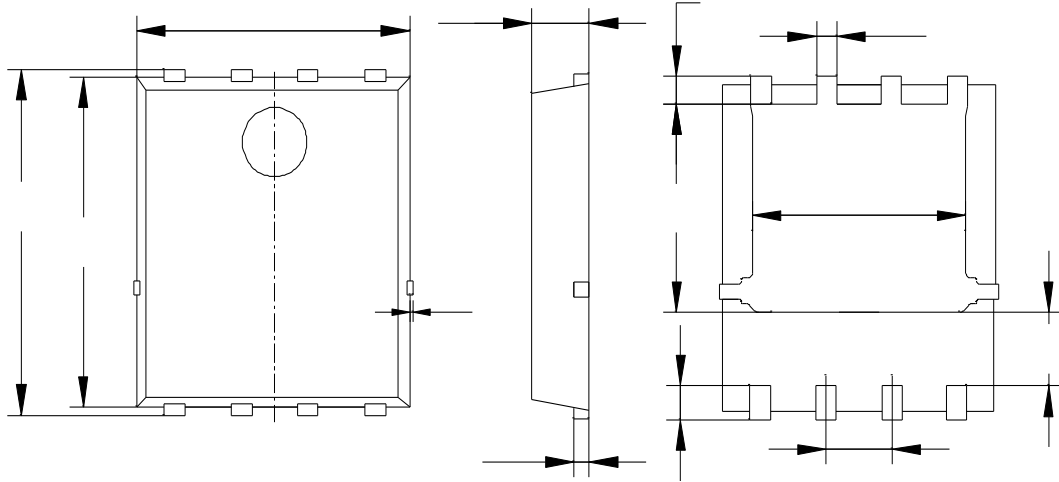


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms



Figure 4. Diode reverse recovery test circuit & waveforms

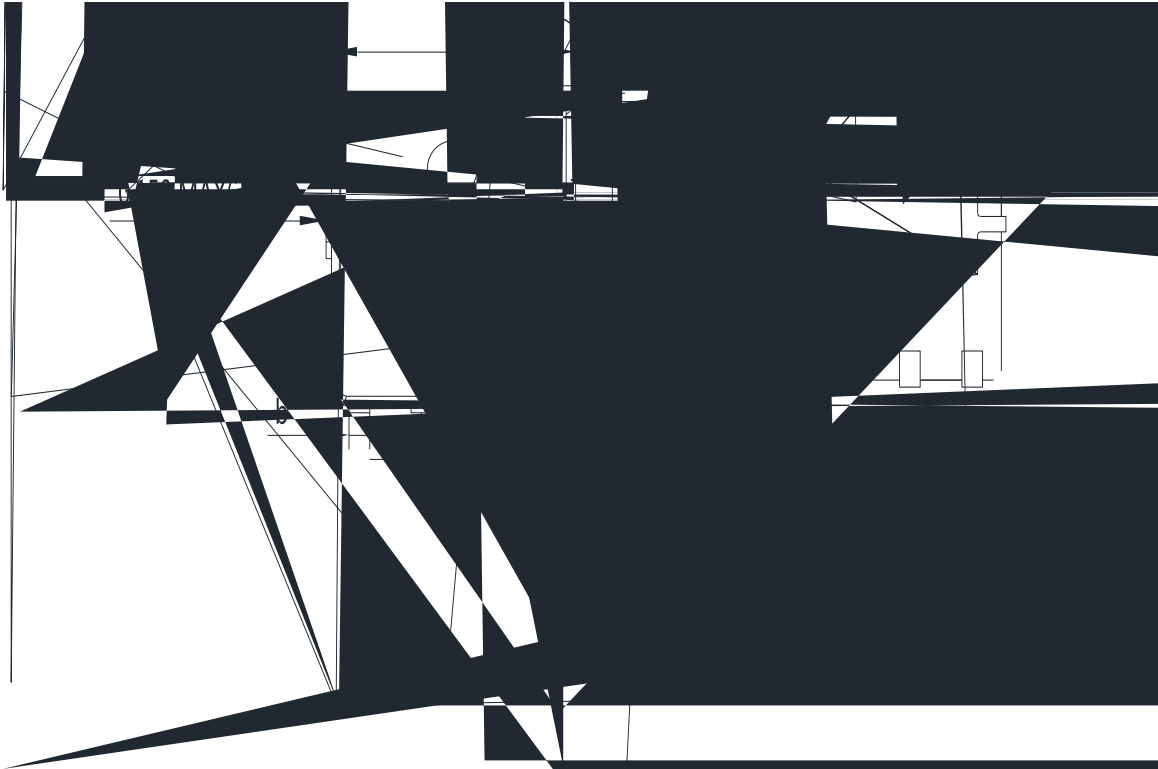
Package Information



| Symbol | mm | | |
|--------|----------|-------|-------|
| | Min | Nom | Max |
| A | 1.00 | 1.10 | 1.20 |
| b | 0.30 | 0.40 | 0.50 |
| c | 0.154 | 0.254 | 0.354 |
| D1 | 5.00 | 5.20 | 5.40 |
| D2 | 3.80 | 4.10 | 4.25 |
| e | 1.17 | 1.27 | 1.37 |
| E1 | 5.95 | 6.15 | 6.35 |
| E2 | 5.66 | 5.86 | 6.06 |
| E4 | 3.52 | 3.72 | 3.92 |
| H | 0.40 | 0.50 | 0.60 |
| L | 0.30 | 0.60 | 0.70 |
| L1 | 0.12 REF | | |
| K | 1.15 | 1.30 | 1.45 |

Version 1: PDFN5*6-P package outline dimension

Package Information



| Symbol | mm | | |
|--------|-----------|------|------|
| | Min | Nom | Max |
| A | 0.8 | 0.9 | 1.0 |
| A1 | 0 | 0.03 | 0.05 |
| b | 0.35 | 0.42 | 0.49 |
| c | 0.254 REF | | |
| D | 4.9 | 5.0 | 5.1 |
| F | 1.40 REF | | |
| E | 5.7 | 5.8 | 5.9 |
| e | 1.27 BSC | | |
| H | 5.95 | 6.08 | 6.20 |
| L1 | 0.10 | 0.14 | 0.18 |
| G | 0.60 REF | | |
| K | 4.00 REF | | |

Version 2: PDFN5*6-K package outline dimension

Ordering Information

| Package Type | Units/ Reel | Reels / Inner Box | Units/ Inner Box | Inner Boxes/ Carton Box | Units/ Carton Box |
|--------------|-------------|-------------------|------------------|-------------------------|-------------------|
| PDFN5*6-P | 5000 | 2 | 10000 | 5 | 50000 |
| PDFN5*6-K | 5000 | 2 | 10000 | 5 | 50000 |

Product Information

| Product | Package | Pb Free | RoHS | Halogen Free |
|------------|---------|---------|------|--------------|
| SFS08R07GF | PDFN5*6 | yes | yes | yes |

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Oriental Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

For further information on technology, delivery terms and conditions and prices, please contact the Oriental Semiconductor sales representatives (www.orientalsemi.com).

© Oriental Semiconductor Co.,Ltd. All Rights Reserved 