

The GreenMOS[®] high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS[®] Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.



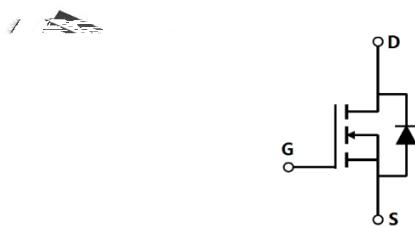
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Parameter	Value	Unit
$V_{DS, \min} @ T_{j(\max)}$	650	V
I_D, pulse	60	A
$R_{DS(ON)}, \text{max} @ V_{GS}=10V$	180	m
Q_g	23.3	nC

Product Name	Package	Marking
OSG60R180KF	TO263	OSG60R180K



Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	600	V
Gate-source voltage	V_{GS}	± 30	V
Continuous drain current ¹⁾ , $T_C=25^\circ\text{C}$	I_D	20	A

Dynamic Characteristics

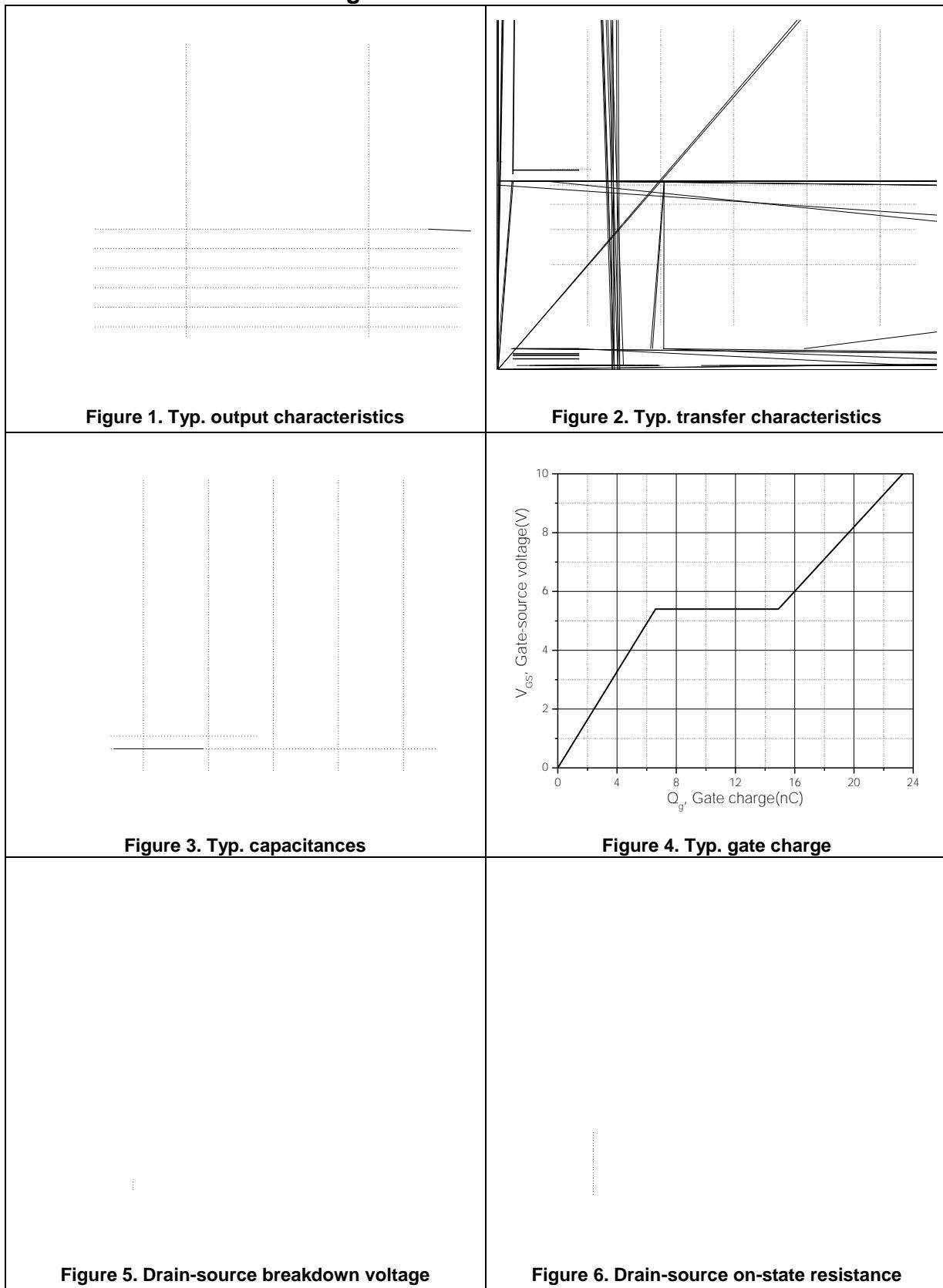
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C _{iss}		1440		pF	V _{GS} =0 V, V _{DS} =50 V, Hz
Output capacitance	C _{oss}		105		pF	
Reverse transfer capacitance	C _{rss}		3.94		pF	
Turn-on delay time	t _{d(on)}		40.3		ns	V _{GS} =10 V, V _{DS} =480 V, R _G =25 I _D =20 A
Rise time	t _r		49.3		ns	
Turn-off delay time	t _{d(off)}		60		ns	
Fall time	t _f		59.2		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q _g		23.3		nC	V _{GS} =10 V, V _{DS} =480 V, I _D =20 A
Gate-source charge	Q _{gs}		6.6		nC	
Gate-drain charge	Q _{gd}		8.3		nC	
Gate plateau voltage	V _{plateau}		5.6		V	

Body Diode Characteristics

Electrical Characteristics Diagrams



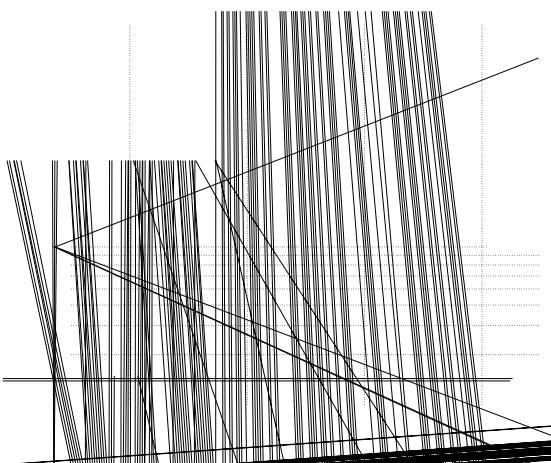


Figure 7. Forward characteristic of body diode

Figure 8. Drain-source on-state resistance

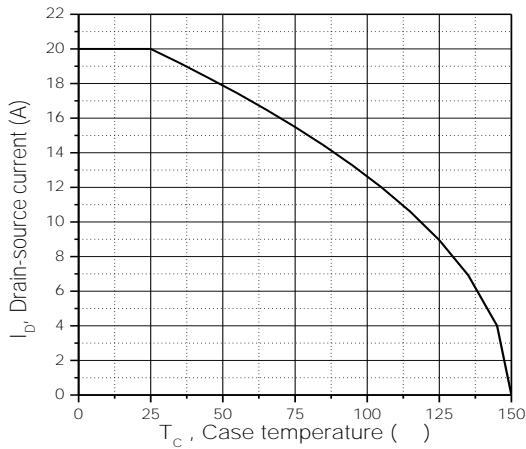


Figure 9. Drain current

Figure 10. Safe operation area Tc=25 °C

Test circuits and waveforms

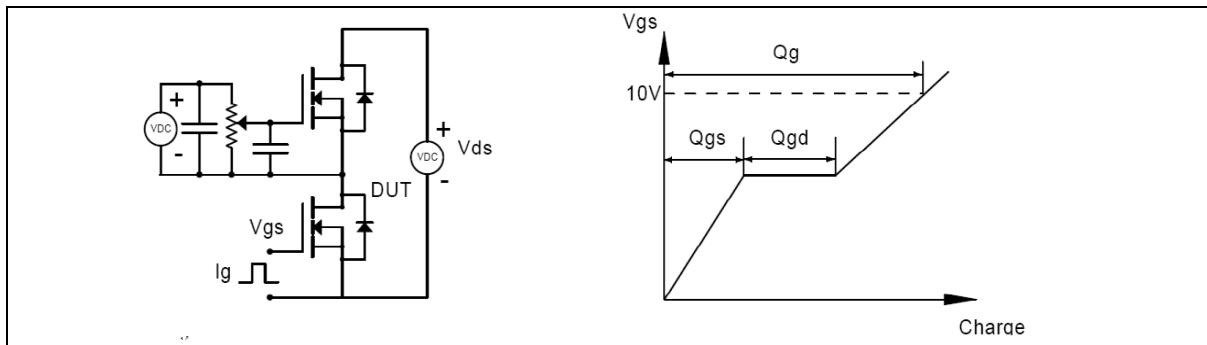


Figure 1. Gate charge test circuit & waveform

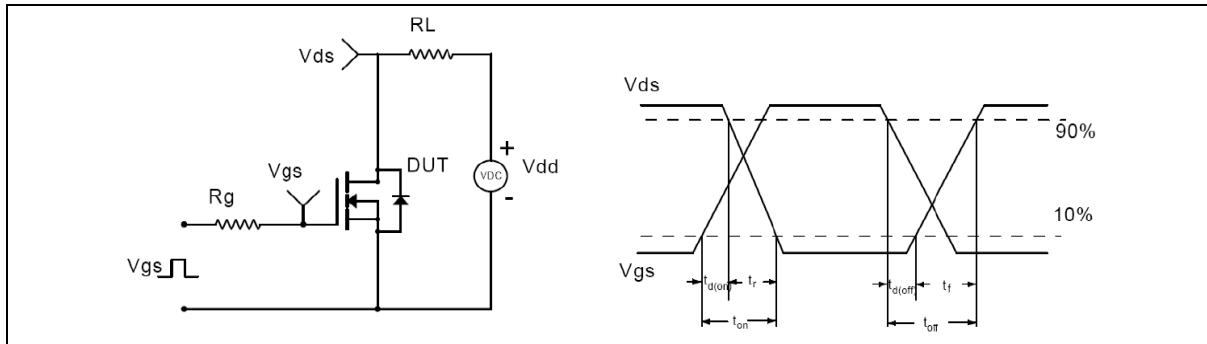


Figure 2. Switching time test circuit & waveforms

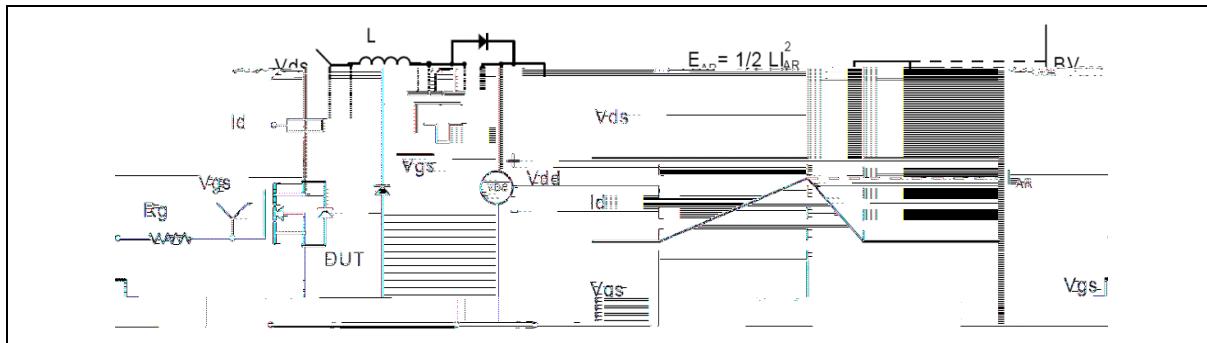


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

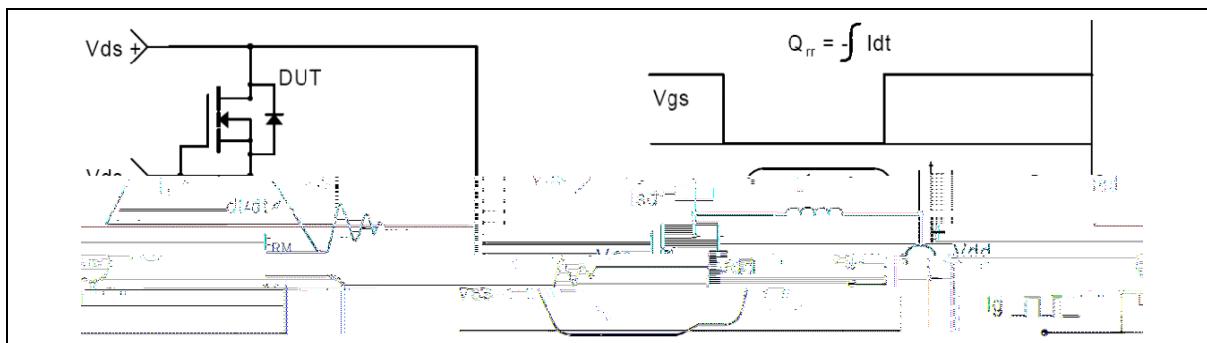
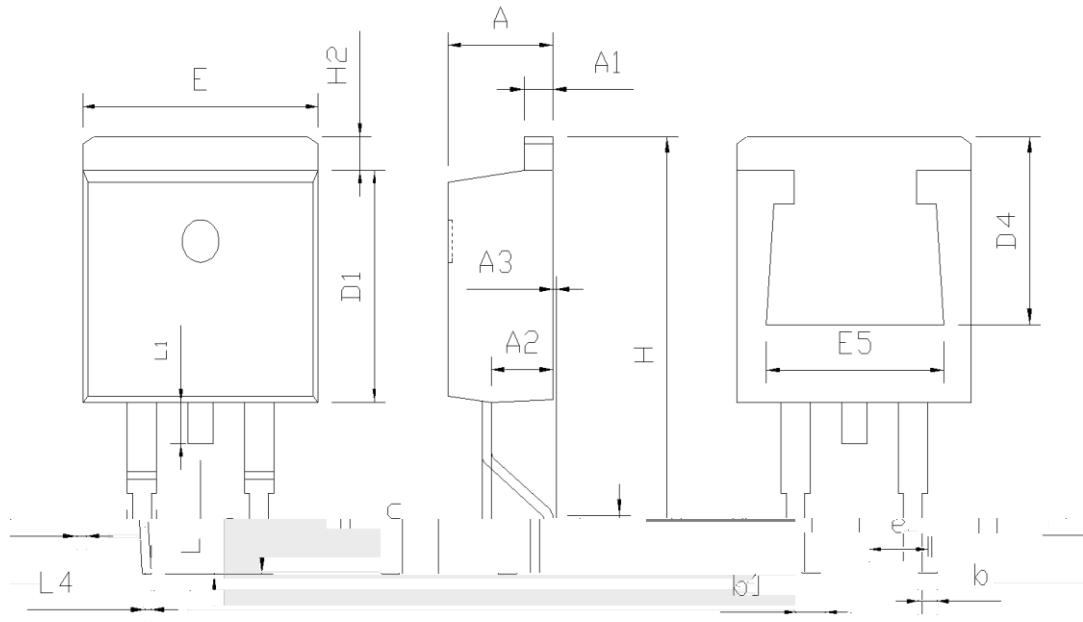


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.37	4.57	4.77
A1	1.22	1.27	1.42
A2	2.49	2.69	2.89
A3	0.00	0.13	0.25
b	0.70	0.81	0.96
b1	1.17	1.27	1.47
c	0.30	0.38	0.53
D1	8.50	8.70	8.90
D4	6.60	-	-
E	9.86	10.16	10.36
E5	7.06	-	-
e	2.54 BSC		
H	14.70	15.10	15.50
H2	1.07	1.27	1.47
L	2.00	2.30	2.60
L1	1.40	1.55	1.70
L4	0.25 BSC		
	0°	5°	9°

Version 1: TO263-P outline dimension

Ordering Information

Package Type	Units/Reel	Reels / Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO263-P	800	1	800	5	4000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG60R180KF	TO263	yes	yes	yes

