

The GreenMOS[®] high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS[®] Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.



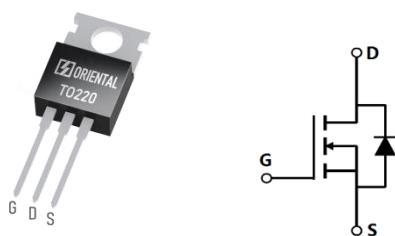
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Parameter	Value	Unit
$V_{DS, \min} @ T_{j(\max)}$	650	V
I_D, pulse	12	A
$R_{DS(ON)}, \text{max} @ V_{GS}=10V$	1.2	
Q_g	6.8	nC

Product Name	Package	Marking
OSG60R1K2PF	TO220	OSG60R1K2P



Absolute Maximum Ratings at $T_j=25$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	600	V
Gate-source voltage	V_{GS}	± 30	V
Continuous drain current ¹⁾ , $T_c=25$ °C	I_D	4	A
Continuous drain current ¹⁾ , $T_c=100$ °C		2.5	
Pulsed drain current ²⁾ , $T_c=25$ °C	$I_{D, \text{pulse}}$	12	A
Continuous diode forward current ¹⁾ , $T_c=25$ °C	I_S	4	A
Diode pulsed current ²⁾ , $T_c=25$ °C	$I_{S, \text{pulse}}$	12	A
Power dissipation ³⁾ , $T_c=25$ °C	P_D	28.4	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	100	mJ
MOSFET dv/dt ruggedness, V_{DS}	dv/dt	50	V/ns
Reverse diode dv/dt, V_{DS}	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	R	4.4	°C/W
Thermal resistance, junction-ambient ⁴⁾	R	62	°C/W

Electrical Characteristics at $T_j=25$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	600			V	$V_{GS}=0$ V, $I_D=250$ A
		650	750			$V_{GS}=0$ V, I_D , $T_j=150$ °C
Gate threshold voltage	$V_{GS(\text{th})}$	2.0		4.0	V	$V_{DS}=V_{GS}$, $I_D=250$ A

Electrical Characteristics Diagrams

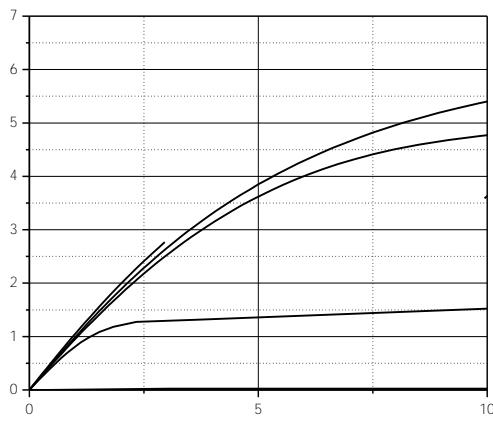


Figure 1. Typ. output characteristics

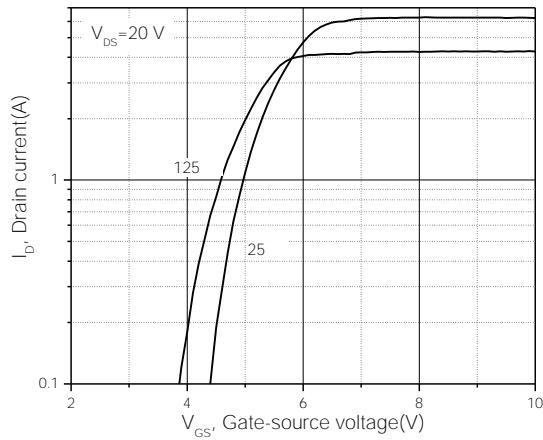


Figure 2. Typ. transfer characteristics

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Figure 3. Typ. capacitances

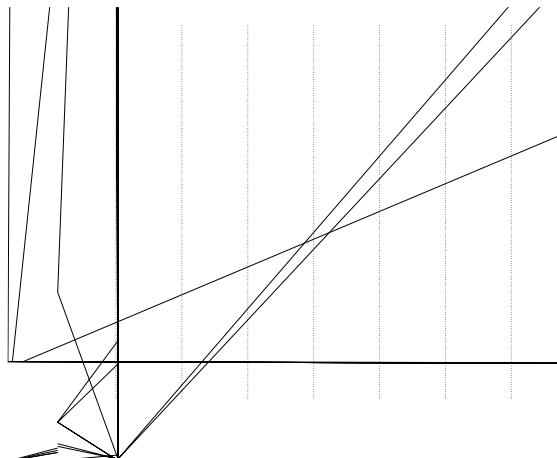


Figure 4. Typ. gate charge

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Figure 5. Drain-source breakdown voltage

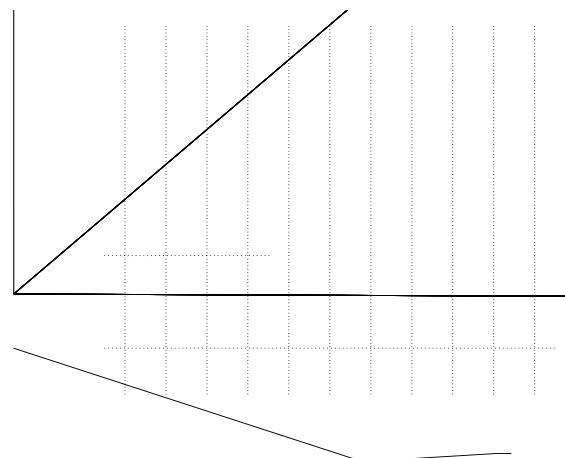
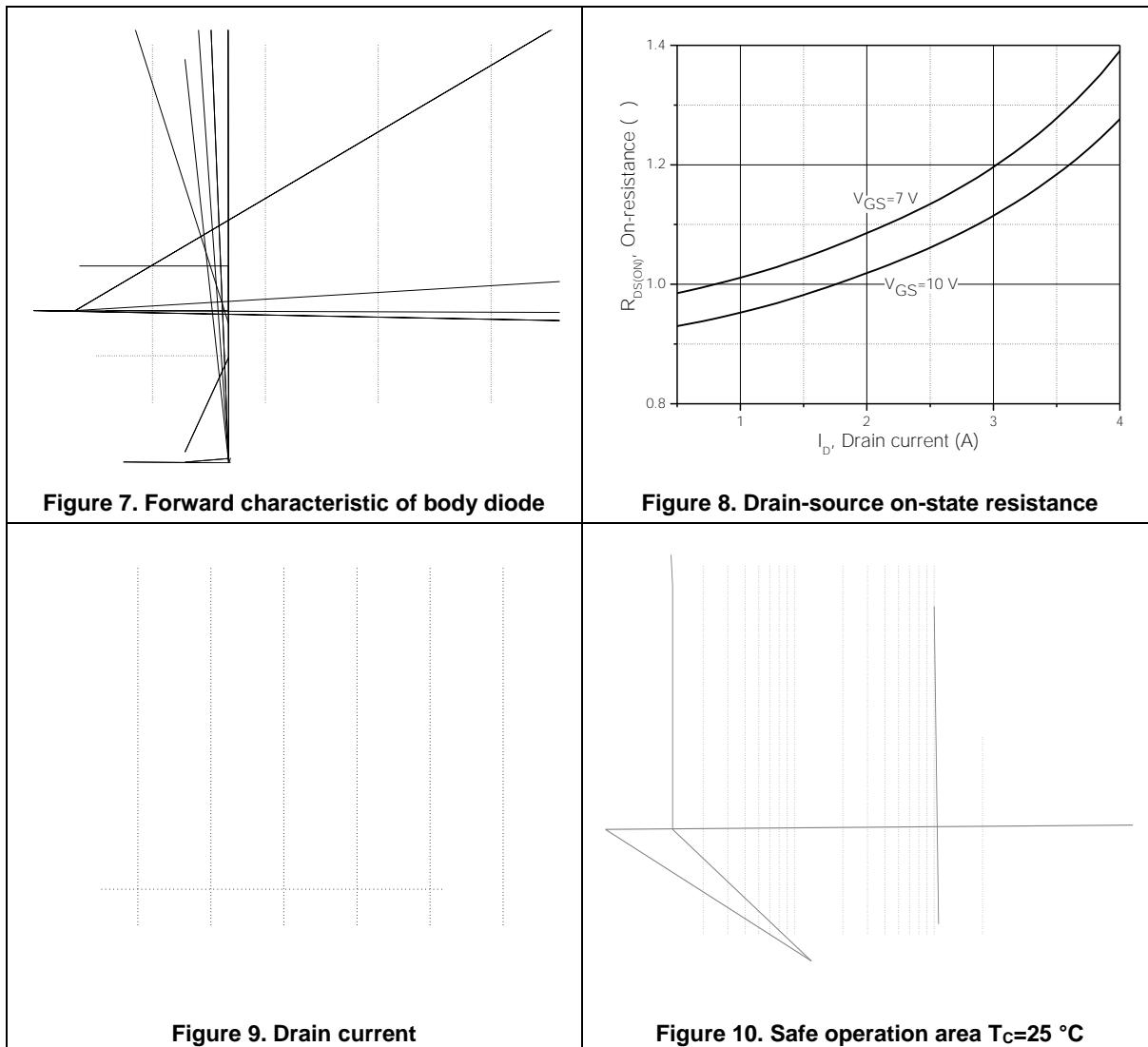


Figure 6. Drain-source on-state resistance



Test circuits and waveforms

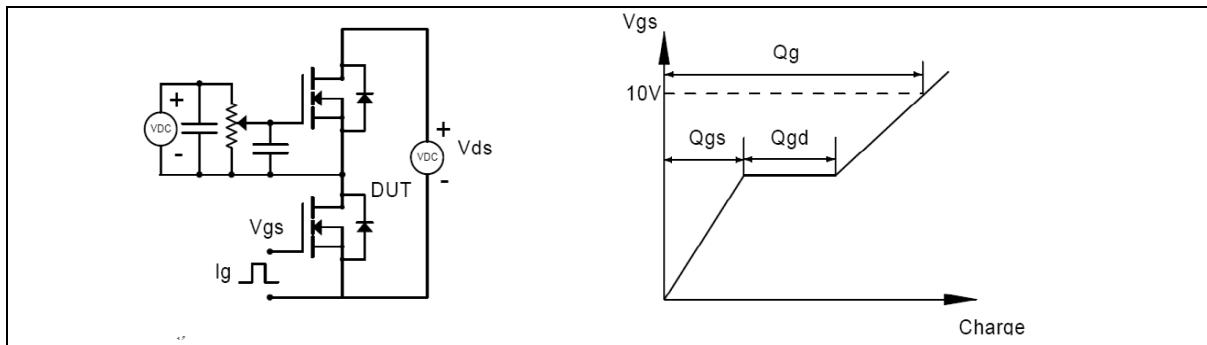


Figure 1. Gate charge test circuit & waveform

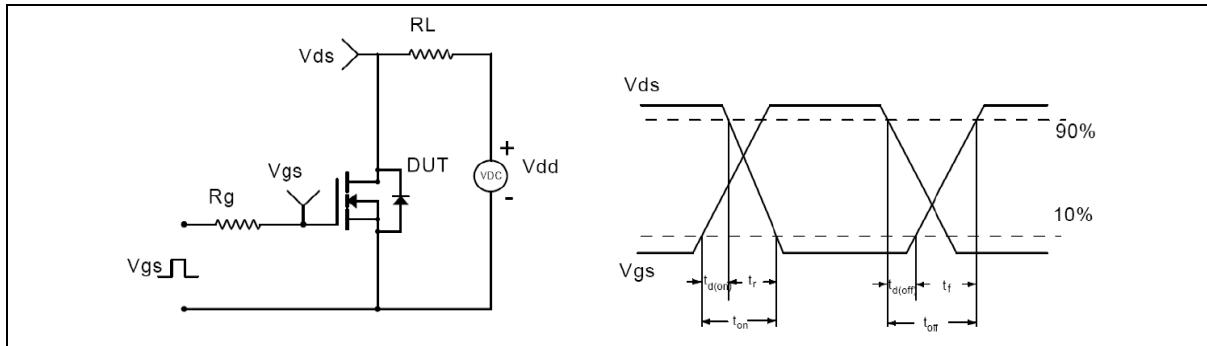


Figure 2. Switching time test circuit & waveforms

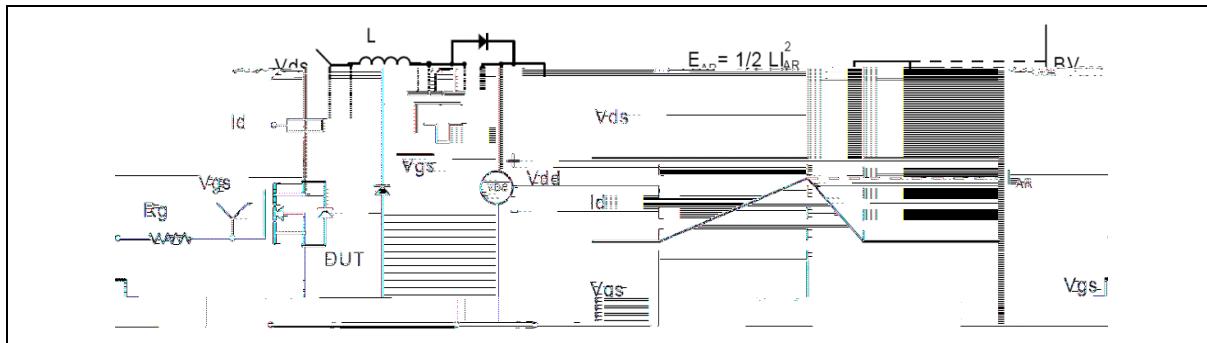


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

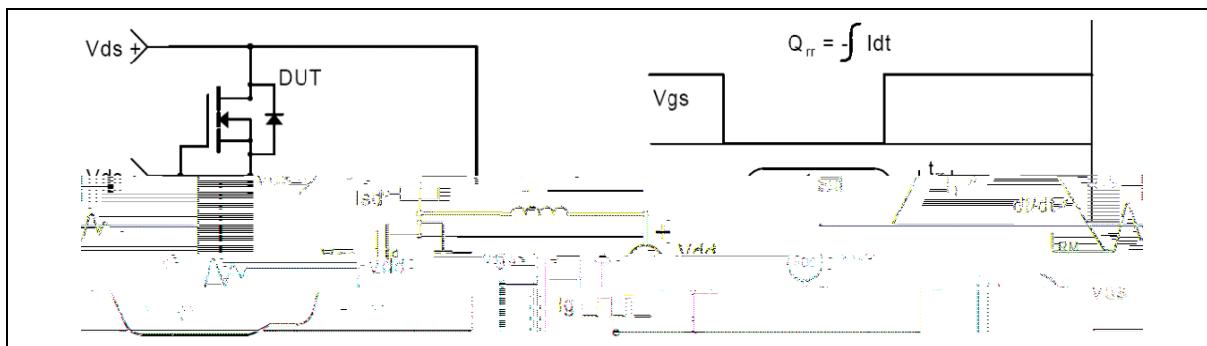
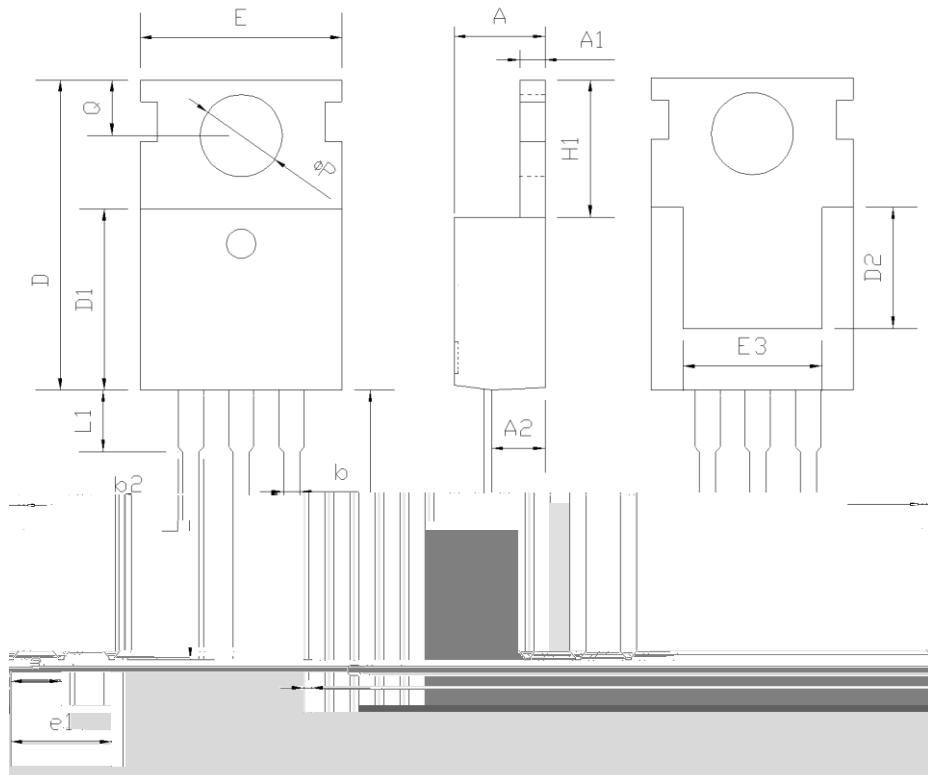


Figure 4. Diode reverse recovery test circuit & waveforms

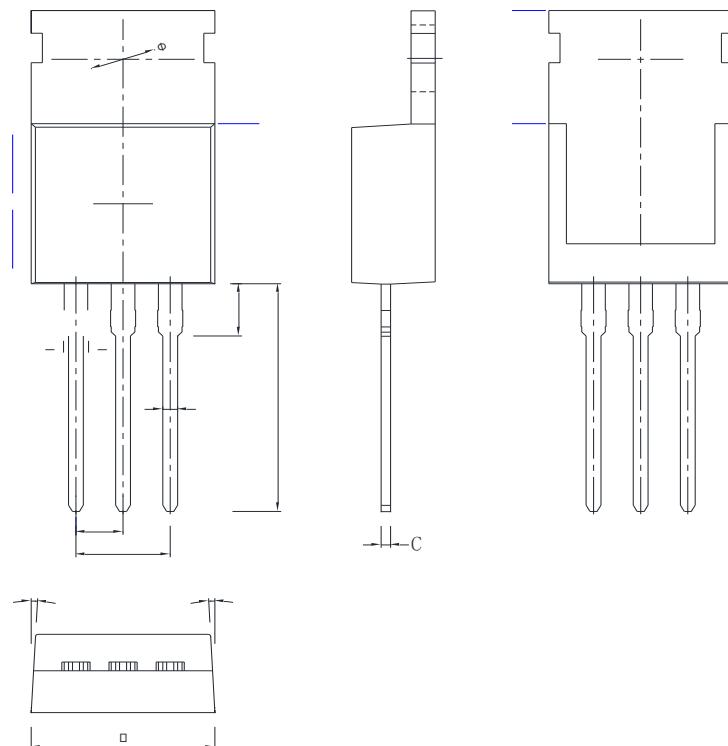
Package Information



Symbol	mm		
	Min	Nom	Max
A	4.37	4.57	4.77
A1	1.25	1.30	1.45
A2	2.20	2.40	2.60
b	0.70	0.80	0.95
b2	1.17	1.27	1.47
c	0.40	0.50	0.65
D	15.10	15.60	16.10
D1	8.80	9.10	9.40
D2	5.50	-	-
E	9.70	10.00	10.30
E3	7.00	-	-
e	2.54 BSC		
e1	5.08 BSC		
H1	6.25	6.50	6.85
L	12.75	13.50	13.80
L1	-	3.10	3.40
	3.40	3.60	3.80
Q	2.60	2.80	3.00

Version 1: TO220-C package outline dimension

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.40	4.50	4.60
A1	1.27	1.30	1.33
A2	2.30	2.40	2.50
b	0.70	-	0.90
b1	1.27	-	1.40
c	0.45	0.50	0.60
D	15.30	15.70	16.10
D1	9.10	9.20	9.30
D2	13.10	-	13.70
E	9.70	9.90	10.20
E1	7.80	8.00	8.20
e	2.54 BSC		
e1	5.08 BSC		
H1	6.30	6.50	6.70
L	12.78	13.08	13.38
L1	-	-	3.50
L2	4.60 REF		
	3.55	3.60	3.65
Q	2.73	-	2.87
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Version 2: TO220-J package outline dimension

Ordering Information

Package Type	Units/ Tube	Tubes/ Inner Box	Units/ Inner Box
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