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The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

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Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C _{iss}		4321		pF	V _{GS} =0 V, V _{DS} =50 V, kHz
Output capacitance	C _{oss}		283.3		pF	
Reverse transfer capacitance	C _{rss}		5.2		pF	
Turn-on delay time	t _{d(on)}		89.7		ns	V _{GS} =10 V, V _{DS} =400 V, R _G =25 I _D =30 A
Rise time	t _r		104.4		ns	
Turn-off delay time	t _{d(off)}		143.6		ns	
Fall time	t _f		73.2		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q _g		60.5		nC	V _{GS} =10 V, V _{DS} =400 V, I _D =30 A
Gate-source charge	Q _{gs}		20.1		nC	
Gate-drain charge	Q _{gd}		16.3		nC	
Gate plateau voltage	V _{plateau}		5.6		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V _{SD}			1.4	V	I _S =53 A, V _{GS} =0 V
Reverse recovery time	t _{rr}		532		ns	I _S =30 A,
Reverse recovery charge	Q _{rr}		10.5		C	
Peak reverse recovery current	I _{rrm}		36.1		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.

Electrical Characteristics Diagrams

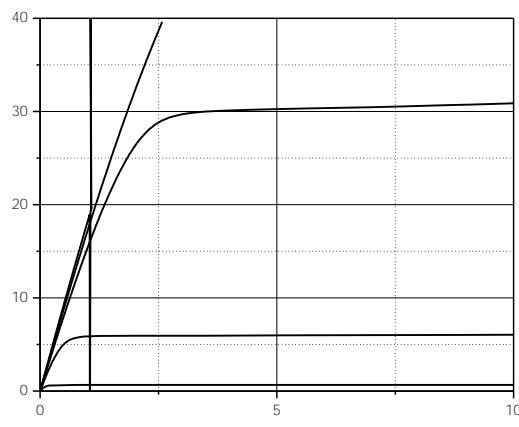


Figure 1. Typ. output characteristics

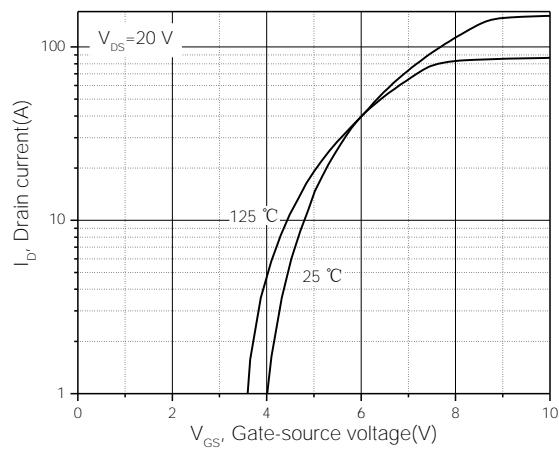


Figure 2. Typ. transfer characteristics

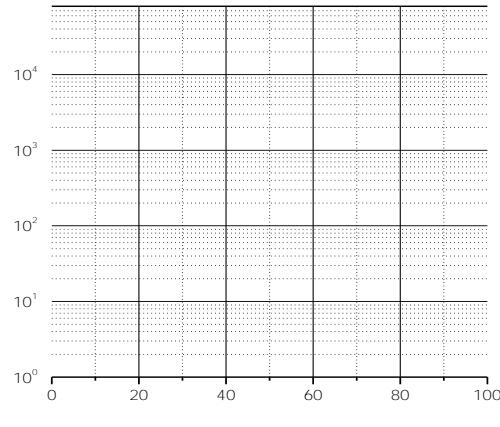


Figure 3. Typ. capacitances

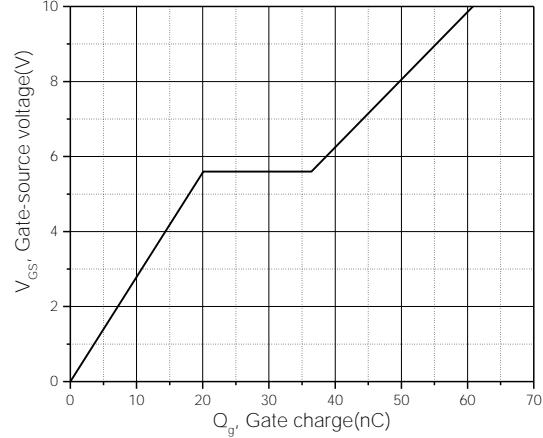


Figure 4. Typ. gate charge

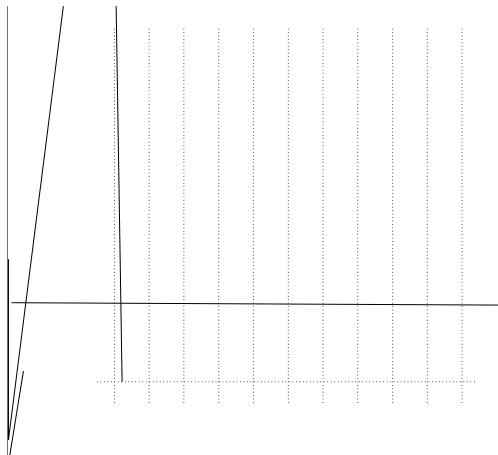


Figure 5. Drain-source breakdown voltage

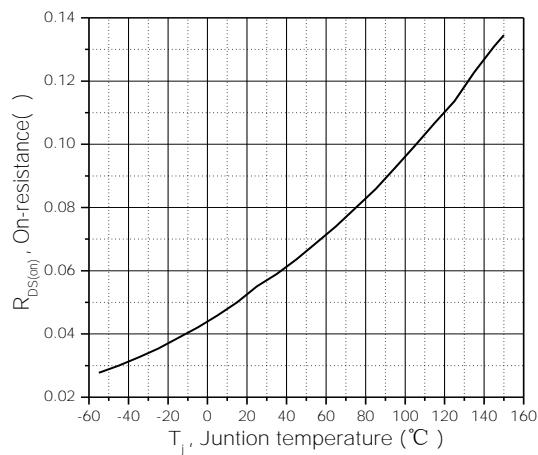


Figure 6. Drain-source on-state resistance

**Figure 7. Forward characteristic of body diode****Figure 8. Drain-source on-state resistance****Figure 9. Drain current****Figure 10. Safe operation area Tc=25**

Test circuits and waveforms

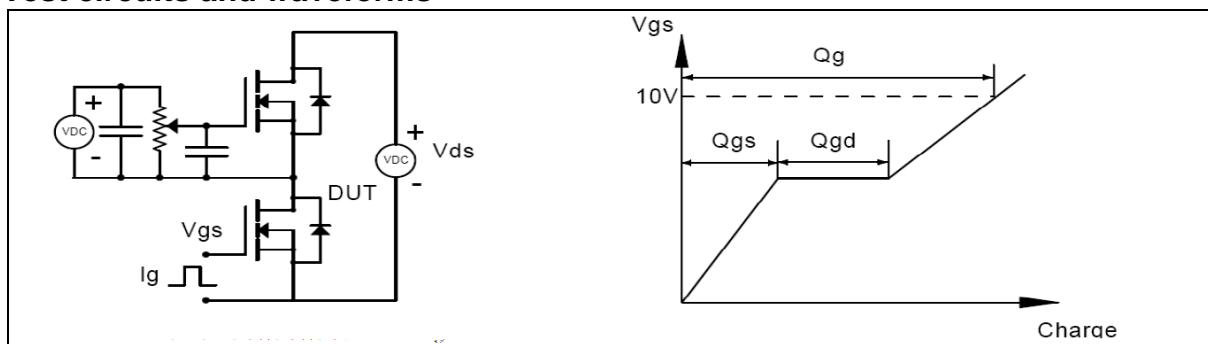


Figure 1. Gate charge test circuit & waveform

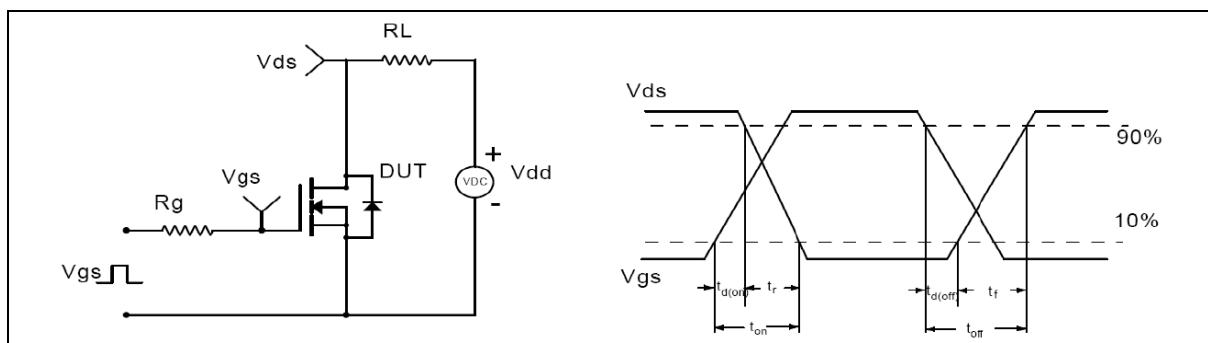


Figure 2. Switching time test circuit & waveforms

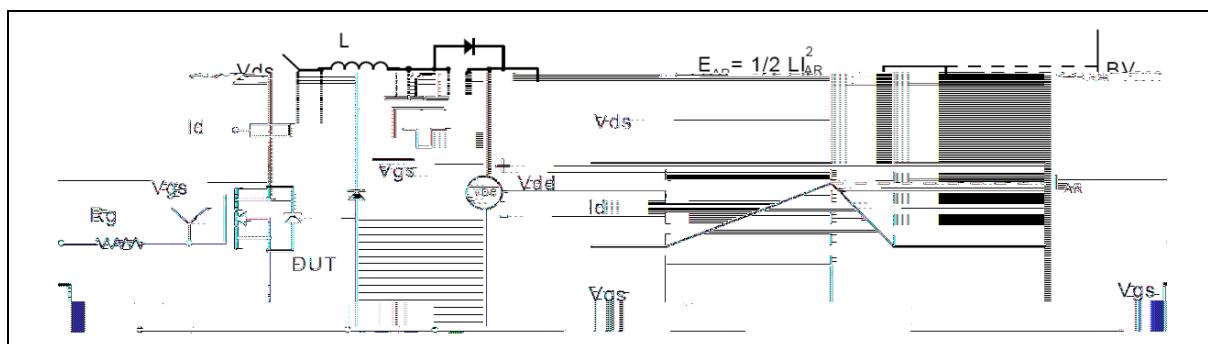


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

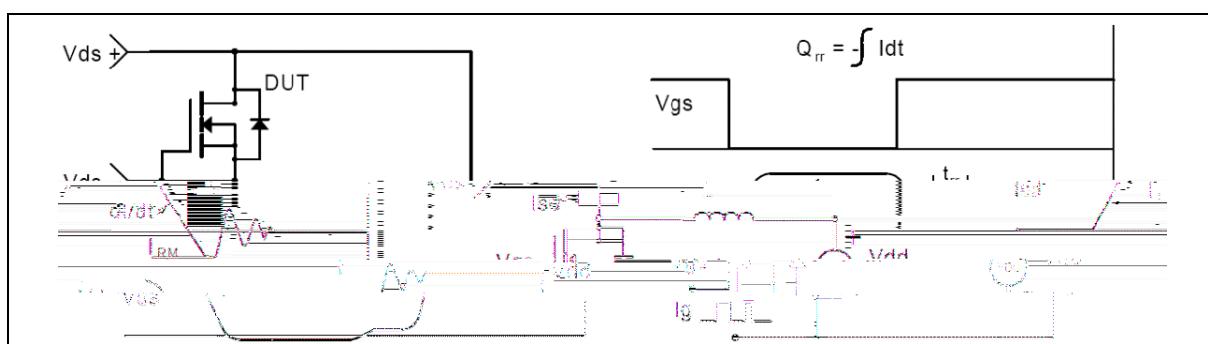
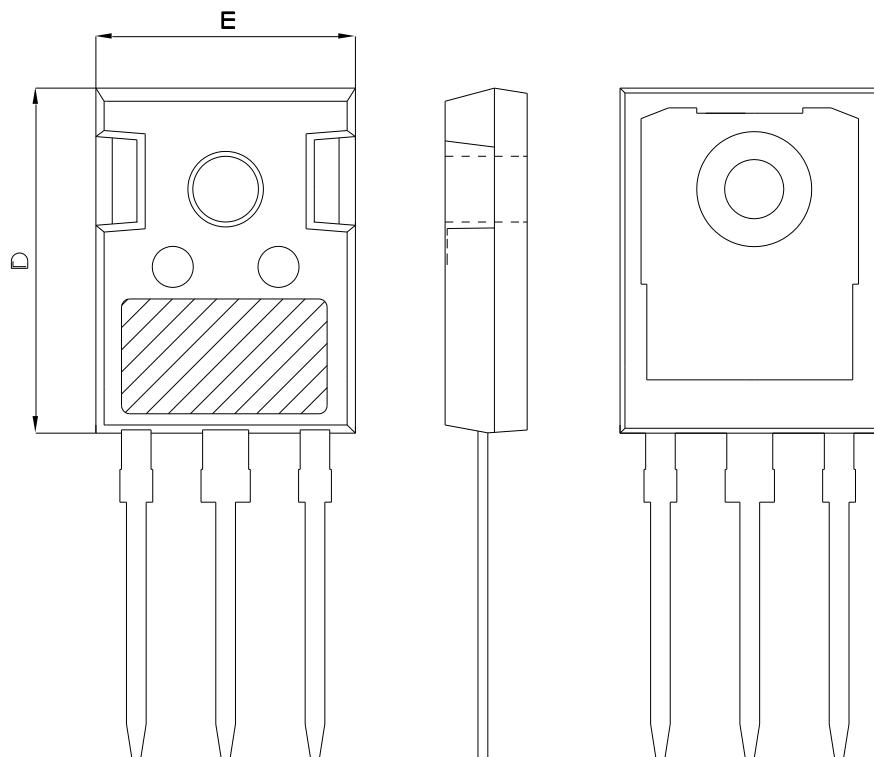


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.80	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44 BSC		
L	19.82	19.92	20.22
L1	-	-	4.30
	3.40	3.60	3.80
	-	-	7.30
S	6.15 BSC		

Version1: TO247-C package outline dimension

Ordering Information

Package Type	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO247-C	30	11	330	6	1980

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG65R069HF	TO247	yes	yes	yes

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