

The GreenMOS<sup>®</sup> high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

SGD F ddmL NR R rd tr m nosti hyc en ts rv lsgtrf bg bsd m str sn bgtud ff drr lud DL H rs mc cr - l m d rx sn trd en rl kd onvd r took rxsdl r sn l dds gd ansg ddb l m bx mc DL H rs mc cr -

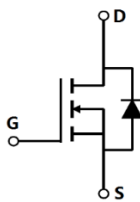
GreenMOS<sup>®</sup>



- Knv Q<sub>CR</sub> NM( ENL
- Dws dl dk knv rv lsgtrf knrr
- Dvbd kns rs alts mc t m l tsx
- KDC kf gstrf
- Bg fd
- @ osd
- Sdkbnl onvd
- Rd ud onvd
- Rnk .TOR

Parameter	Value	Unit
V <sub>DS, min</sub> @ T <sub>j(max)</sub>	700	V
I <sub>D, pulse</sub>	24	A
R <sub>DS(ON), max</sub> @ V <sub>GS</sub> =10V	600	l
Q <sub>g</sub>	12.7	nC

Product Name	Package	Marking
OSG65R600DSF	TO252	OSG65R600DS

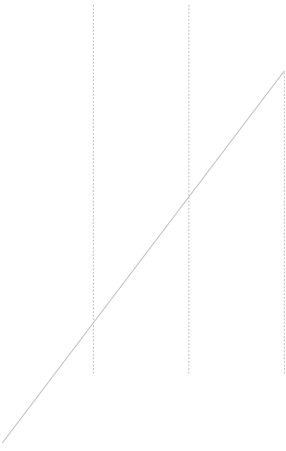

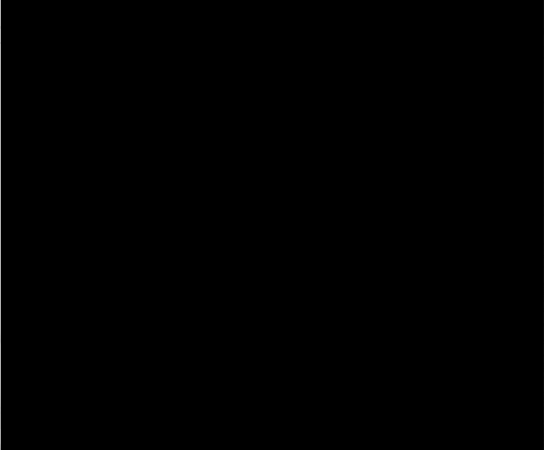
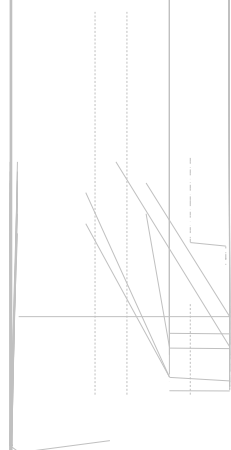

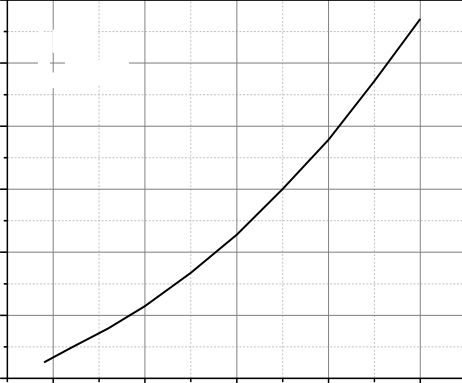


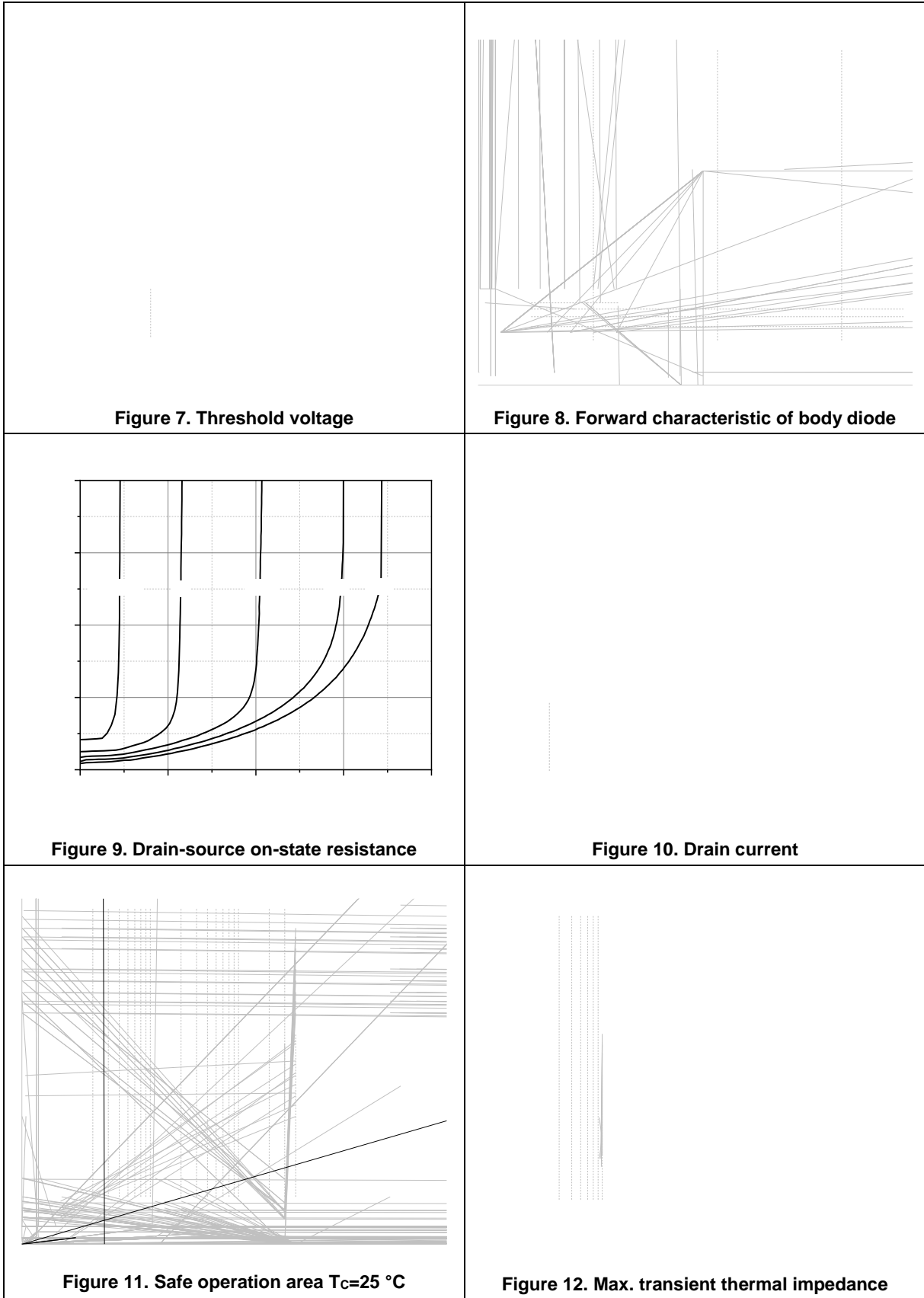


**Dynamic Characteristics**

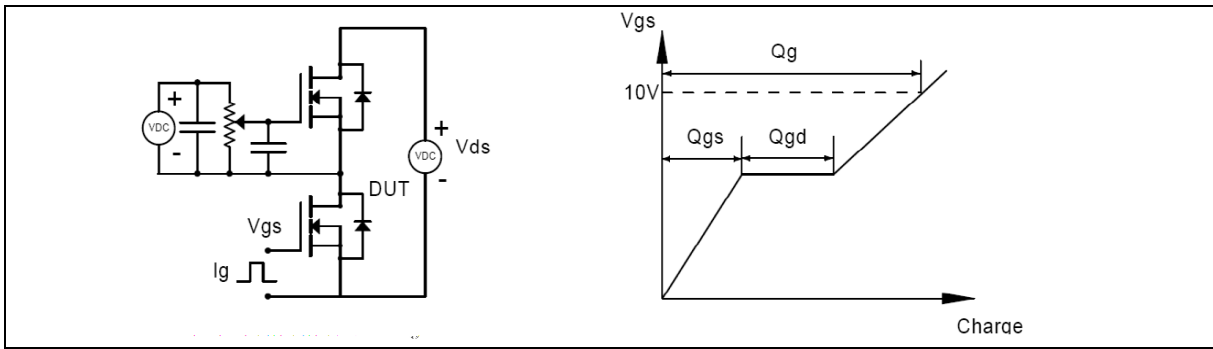
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	$C_{iss}$		551		pF	$V_{GS}=0\text{ V}$ , $V_{DS}=50\text{ V}$ , 000 kHz
Output capacitance	$C_{oss}$		41		pF	
Reverse transfer capacitance	$C_{rss}$		1.6		pF	
Turn-on delay time	$t_{d(on)}$		20.9		ns	$V_{GS}=10\text{ V}$ , $V_{DS}=400\text{ V}$ , $R_G=2$ $I_D=4\text{ A}$
Rise time	$t_r$		7.6		ns	
Turn-off delay time	$t_{d(off)}$					

**Electrical Characteristics Diagrams**

 <p><b>Figure 1. Typ. output characteristics</b></p>	 <p><b>Figure 2. Typ. transfer characteristics</b></p>
 <p><b>Figure 3. Typ. capacitances</b></p>	 <p><b>Figure 4. Typ. gate charge</b></p>
 <p><b>Figure 5. Drain-source breakdown voltage</b></p>	 <p><b>Figure 6. Drain-source on-state resistance</b></p>



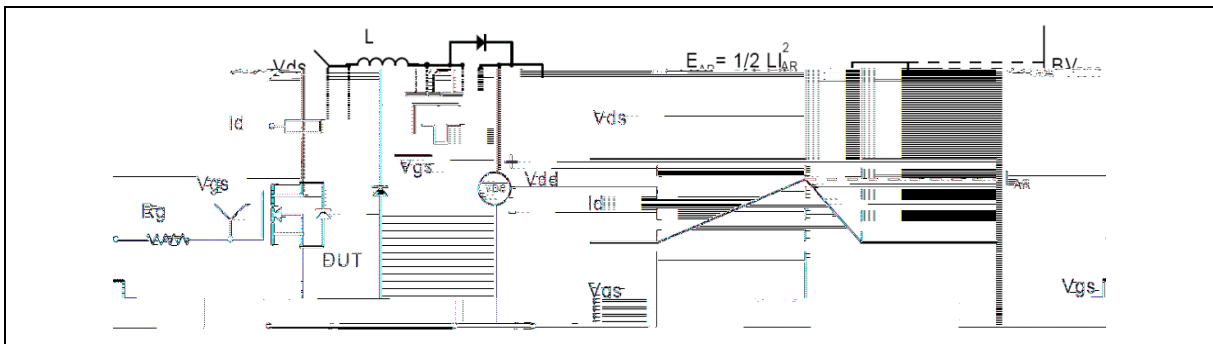
**Test circuits and waveforms**



**Figure 1. Gate charge test circuit & waveform**



**Figure 2. Switching time test circuit & waveforms**

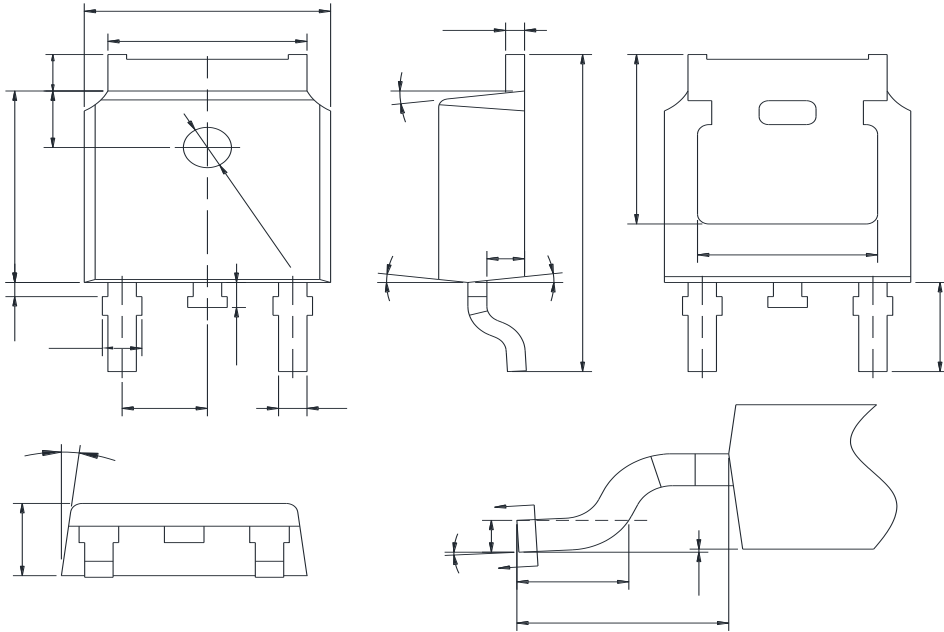


**Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms**



**Figure 4. Diode reverse recovery test circuit & waveforms**

**Package Information**



Symbol	mm		
	Min	Nom	Max
A	2.20	2.30	2.38
A1	0.00	-	0.10
A2	0.90	1.01	1.10
b	0.72	-	0.85
b1	0.71	0.76	0.81
b2	0.72	-	0.90
b3	5.13	5.33	5.46
c	0.47	-	0.60
c1	0.46	0.51	0.56
c2	0.47	-	0.60
D	6.00	6.10	6.20
D1	5.25	-	-
E	6.50	6.60	6.70
E1	4.70	-	-
e	2.186	2.286	2.386
H	9.80	10.10	10.40
L	1.40	1.50	1.70
L1	2.90REF		
L2	0.508BSC		
L3	0.90	-	1.25
L4	0.60	0.80	1.00
L5	0.15	-	0.75
L6	1.80REF		
	0'	-	7'
0	4'	6'	8'
1	4'	6'	8'

Version 1: TO252-J package outline dimension

### Ordering Information

Package Type	Units/ Reel	Reels/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO252-J	2500	2	5000	5	25000

### Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG65R600DSF	TO252	yes	yes	yes

Sgd hre l smmf lwdm lmsgh cnbt l dnrsg k lmmn dudns ad df cdc r ft nedd ne  
 bnnc lsmnr n bg bsd h r sbor -V lsg dr odbsn mx dw l okdr n ghns f lwdmgd dlm mx scolt k  
 u kt dr rs sdc gd dlm ncn mx hre l smm df chrf sgd ooko smmnesgd cdultd N ldnr k  
 Rdl lbnrc t bsn gd dax chbk h r mx ncn kv nrcdr ncn k h alrcdr ne mx j hrc hmbk chrf  
 v lsgnt sk l s smm v nrcdr nemm hrc hrf dl dnr nelms kdbst ko nod sx hf gr ne mx sghc  
 o s-

En et sgd hre l smmnm sdbgrmkf x cdktud x sd l r ncn bnnc lsmnr ncn o hrcdr okd rd  
 bnns bssgd N ldnr kRdl lbnrc t bsn r kdr do dr dnr studr [www-n.ltdns.kd.ltdn.l](http://www-n.ltdns.kd.ltdn.l) (-

