The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

GreenMOS®

RoHS

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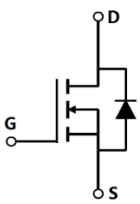
Parameter	Value	Unit
$V_{DS,\ min}$ @ $T_{j(max)}$	700	V
$I_{D,\ pulse}$	15	A
$R_{DS(ON),\ max}$ @ V_{GS}=10V	900	
Q_g	9.0	nC

Product Name	Package	Marking
OSG65R900DEF	TO252	OSG65R900DE



Absolute Maximum Ratings at T_j=25 unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Gate-source voltage	V_{GS}	±30	V
Continuous drain current[1], T_C=25 °C	I_D	5	A
Continuous drain current[1], T_C=100 °C		3.2	
Pulsed drain current[2], T_C=25 °C	$I_{D,\ pulse}$	15	A
Continuous diode forward current[1], T_C=25 °C	I_S	5	A
Diode pulsed current[2], T_C=25 °C	$I_{S,\ pulse}$	15	A
Power dissipation[3] , T_C=25 °C	P_D	37	W
Single pulsed avalanche energy[5]	E_{AS}	130	mJ
MOSFET dv/dt ruggedness, V_{DS}	dv/dt	50	V/ns
Reverse diode dv/dt, V_{DS} SD D	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	R	3.4	°C/W
Thermal resistance, junction-ambient[4]	R	62	°C/W

Electrical Characteristics at T_j=25 unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	650			V	V_{GS}=0 V, I_D=250 A
		700	770			V_{GS}=0 V, I_D , T_j=150 °C
Gate threshold voltage	$V_{GS(th)}$	2.9		3.9	V	V_{DS}=V_{GS}, I_D=250 A
Drain-source on-state resistance	$R_{DS(ON)}$		0.8	0.9		V_{GS}=10 V, I_D=2.5 A
			2.3			V_{GS}=10 V, I_D=2.5 A, T_j=150 °C
Gate-source leakage current	I_{GSS}			100	nA	V_{GS}=30 V
				-100		V_{GS}=-30 V
Drain-source leakage current	I_{DSS}			1	A	V_{DS}=650 V, V_{GS}=0 V

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		408.0		pF	V_{GS}=0 V, V_{DS}=50 V, Hz
Output capacitance	C_{oss}		30.3		pF	
Reverse transfer capacitance	C_{rss}		1.4		pF	
Turn-on delay time	$t_{d(on)}$		27.4		ns	V_{GS}=10 V, V_{DS}=400 V, R_G=2 I_D=2.5 A
Rise time	t_r		12.6		ns	
Turn-off delay time	$t_{d(off)}$					

Electrical Characteristics Diagrams

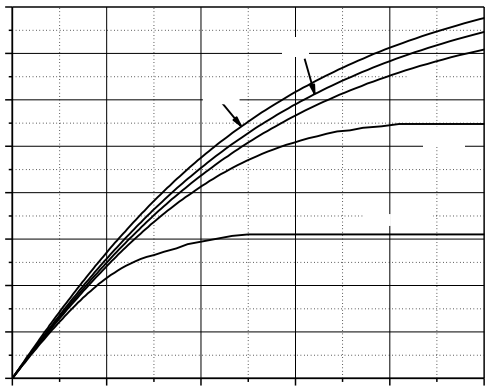


Figure 1. Typ. output characteristics

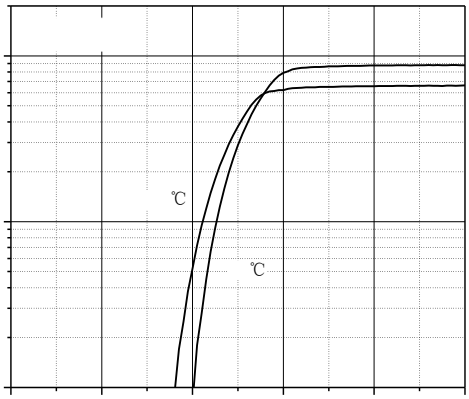


Figure 2. Typ. transfer characteristics

Figure 3. Typ. capacitances

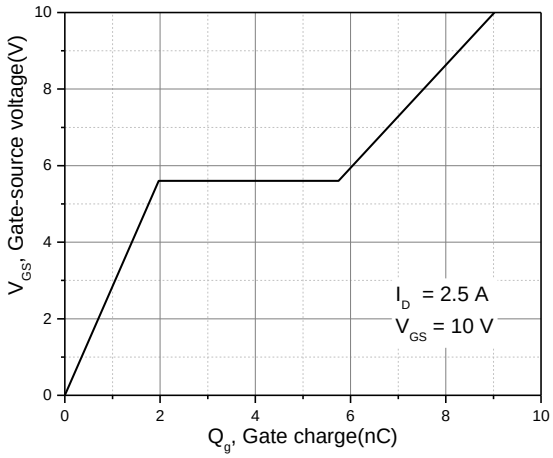


Figure 4. Typ. gate charge

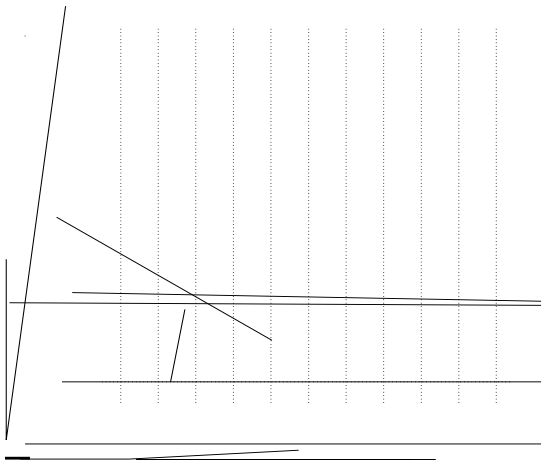


Figure 5. Drain-source breakdown voltage

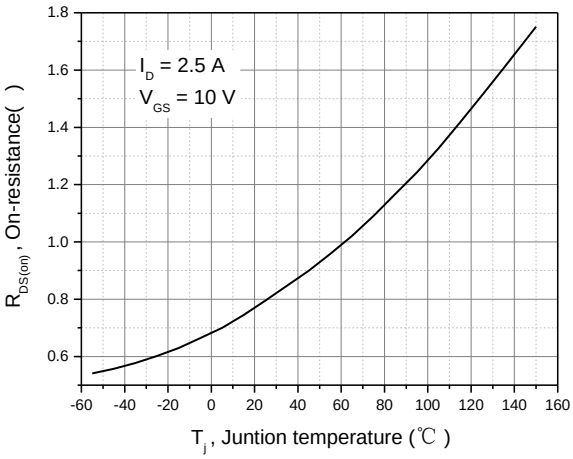


Figure 6. Drain-source on-state resistance

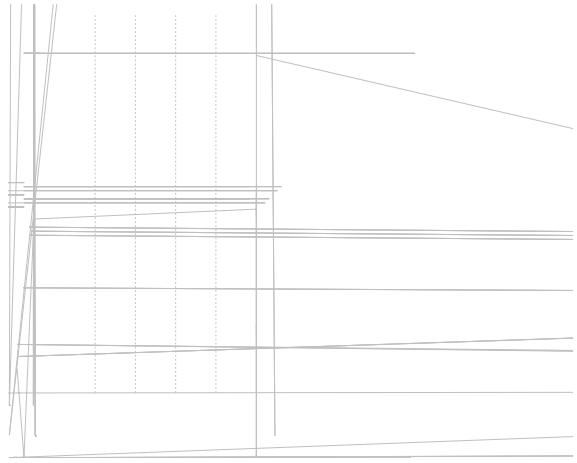


Figure 7. Threshold voltage

Figure 8. Forward characteristic of body diode

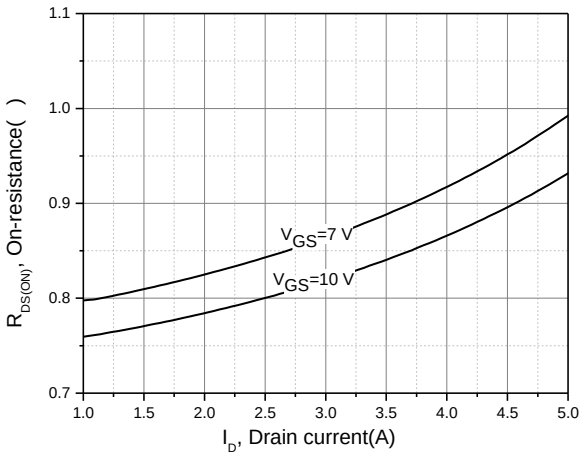


Figure 9. Drain-source on-state resistance

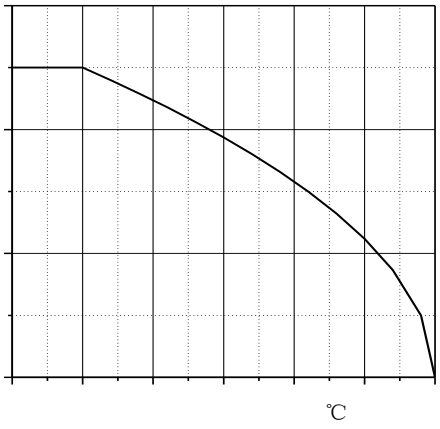


Figure 10. Drain current

Figure 11. Safe operation area T_C=25 °C

Test circuits and waveforms

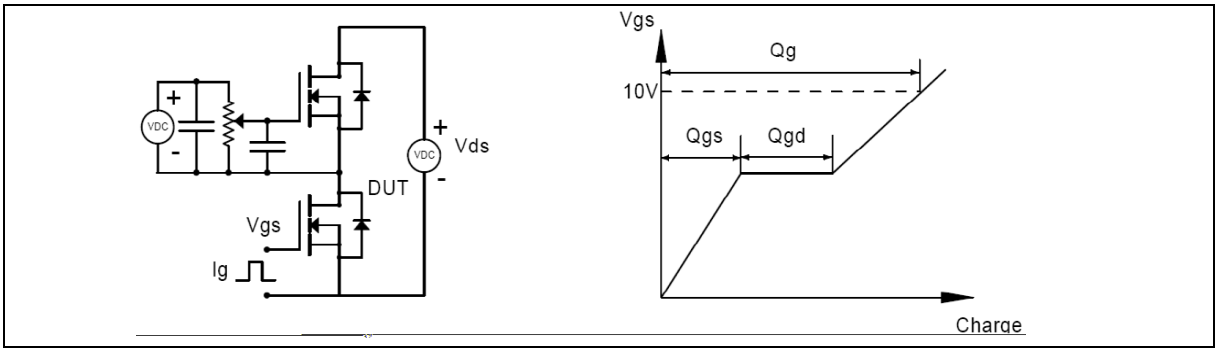


Figure 1. Gate charge test circuit & waveform

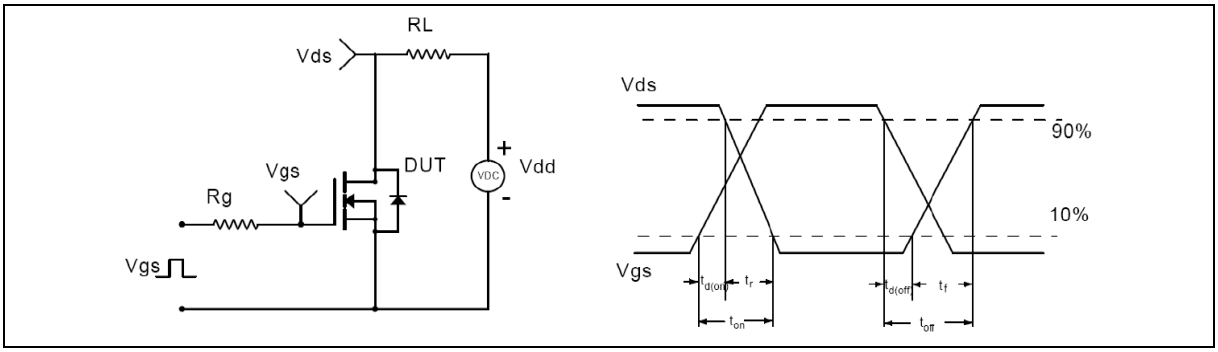


Figure 2. Switching time test circuit & waveforms

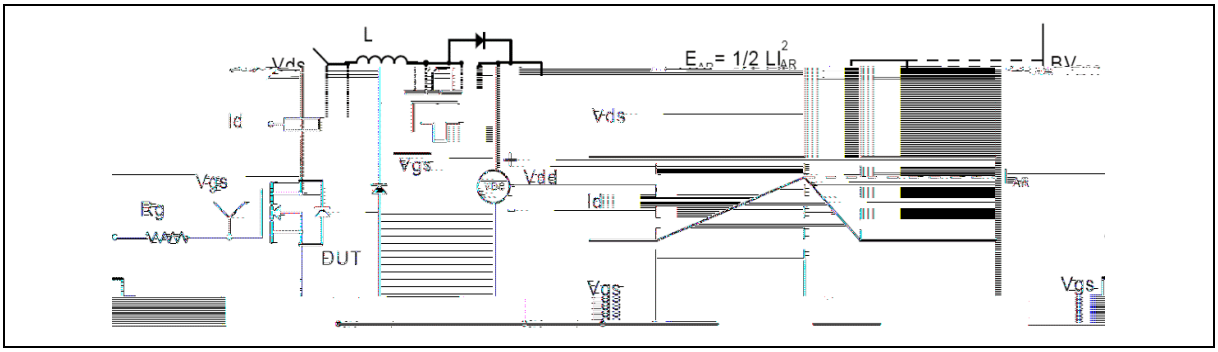


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

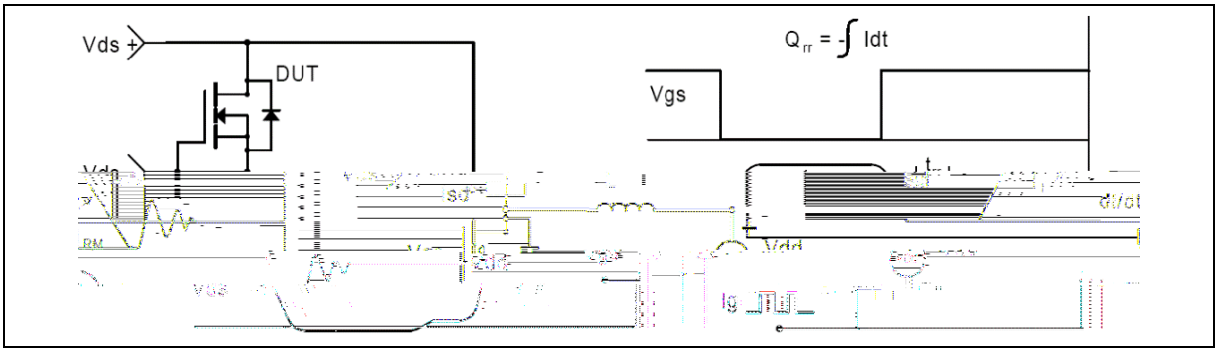
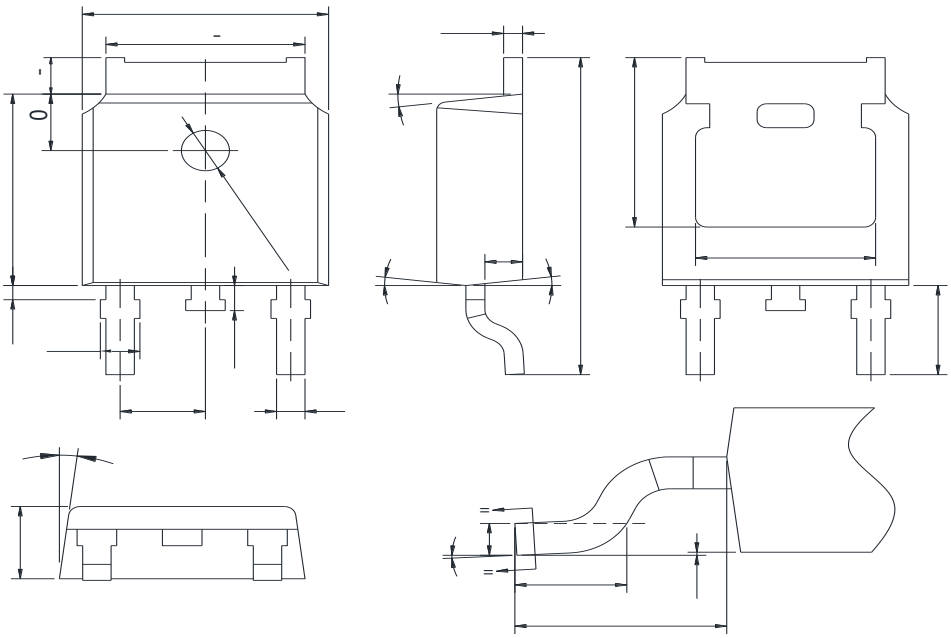


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information

Symbol	mm

Package Information



Symbol	mm		
	Min	Nom	Max
A	2.20	2.30	2.38
A1	0.00	-	0.10
A2	0.90	1.01	1.10
b	0.72	-	0.85
b1	0.71	0.76	0.81
b2	0.72	-	0.90
b3	5.13	5.33	5.46
c	0.47	-	0.60
c1	0.46	0.51	0.56
c2	0.47	-	0.60
D	6.00	6.10	6.20
D1	5.25	-	-
E	6.50	6.60	6.70
E1	4.70	-	-
e	2.186	2.286	2.386
H	9.80	10.10	10.40
L	1.40	1.50	1.70
L1	2.90REF		
L2	0.508BSC		
L3	0.90	-	1.25
L4	0.60	0.80	1.00
L5	0.15	-	0.75
L6	1.80REF		
	0	-	

Version 2: TO252-J package outline dimension

Ordering Information

Package Type	Units/ Reel	Reels/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO252-C	2500	2	5000	5	25000
TO252-J	2500	2	5000	5	25000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG65R900DEF	TO252	yes	yes	yes