

The GreenMOS[®] high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS[®] Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

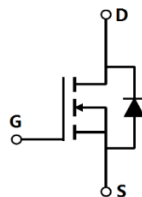




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Parameter	Value	Unit
$V_{DS, min} @ T_{j(max)}$	700	V
$I_{D, pulse}$	15	A
$R_{DS(ON), max} @ V_{GS}=10V$	900	
Q_g	7.6	nC

Product Name	Package	Marking
OSG65R900PF	TO220	OSG65R900P



Absolute Maximum Ratings at $T_j=25$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Gate-source voltage	V_{GS}	± 30	V
Continuous drain current ¹⁾ , $T_C=25$ °C	I_D	5	A
Continuous drain current ¹⁾ , $T_C=100$ °C		3.2	
Pulsed drain current ²⁾ , $T_C=25$ °C	$I_{D, pulse}$	15	A
Continuous diode forward current ¹⁾ , $T_C=25$ °C	I_S	5	A
Diode pulsed current ²⁾ , $T_C=25$ °C	$I_{S, pulse}$	15	A
Power dissipation ³⁾ , $T_C=25$ °C	P_D	37	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	130	mJ
MOSFET dv/dt ruggedness, V_{DS}	dv/dt	50	V/ns
Reverse diode dv/dt, V_{DS}	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		343		pF	$V_{GS}=0\text{ V}$, $V_{DS}=50\text{ V}$, Hz
Output capacitance	C_{oss}		29		pF	
Reverse transfer capacitance	C_{rss}		1.5		pF	
Turn-on delay time	$t_{d(on)}$		15		ns	$V_{GS}=10\text{ V}$, $V_{DS}=380\text{ V}$, $R_G=25$ $I_D=5\text{ A}$
Rise time	t_r		11		ns	
Turn-off delay time	$t_{d(off)}$					

Electrical Characteristics Diagrams

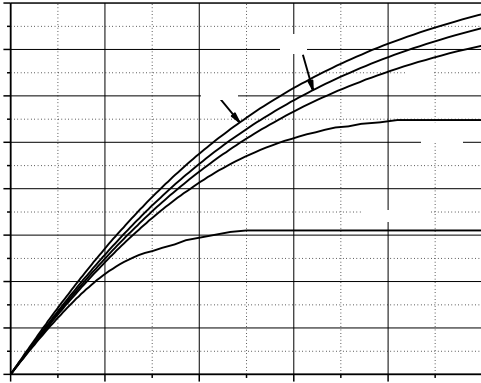


Figure 1. Typ. output characteristics

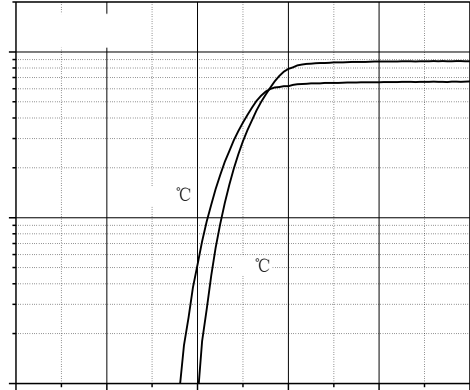


Figure 2. Typ. transfer characteristics

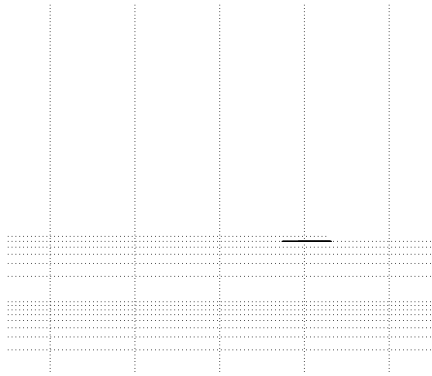


Figure 3. Typ. capacitances

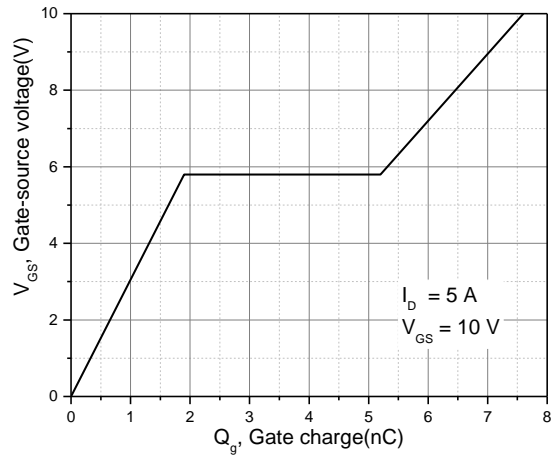


Figure 4. Typ. gate charge

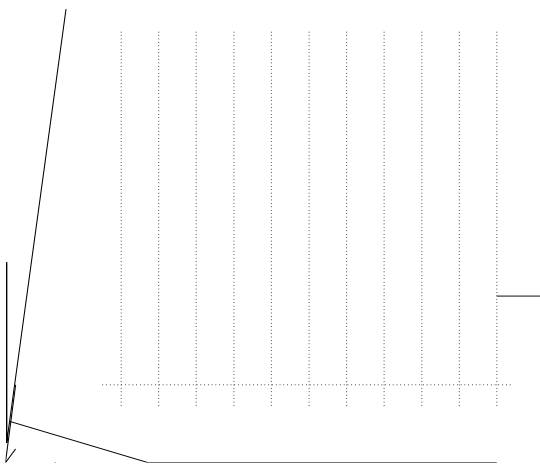


Figure 5. Drain-source breakdown voltage



Figure 6. Drain-source on-state resistance

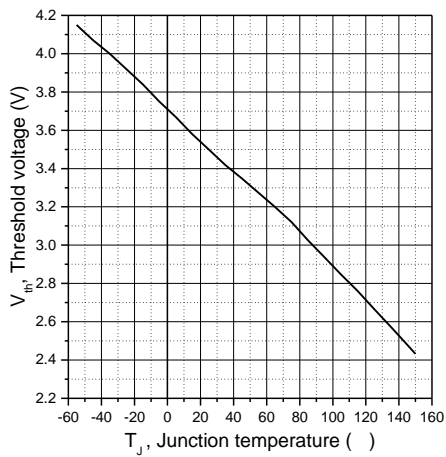


Figure 7. Threshold voltage



Figure 8. Forward characteristic of body diode

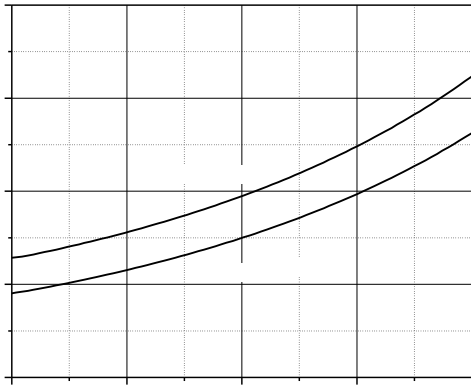


Figure 9. Drain-source on-state resistance

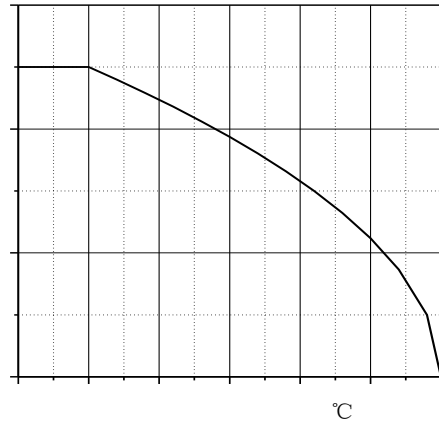


Figure 10. Drain current

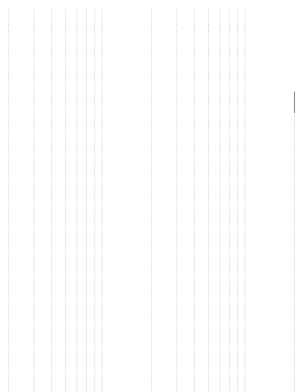

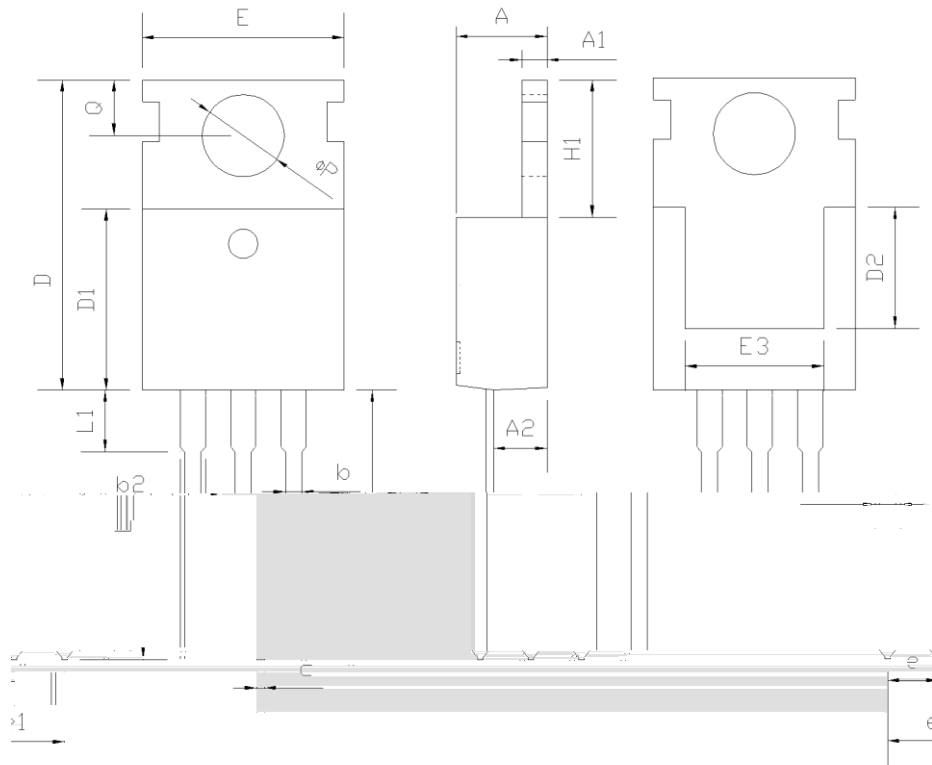


Figure 11. Safe operation area T_c=25 °C

OSG65R900PF
Enhancement Mode N-Channel Power MOSFET 

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.37	4.57	4.77
A1	1.25	1.30	1.45
A2	2.20	2.40	2.60
b	0.70	0.80	0.95
b2	1.17	1.27	1.47
c	0.40	0.50	0.65
D	15.10	15.60	16.10
D1	8.80	9.10	9.40
D2	5.50	-	-
E	9.70	10.00	10.30
E3	7.00	-	-
e	2.54BSC		
e1	5.08BSC		
H1	6.25	6.50	6.85
L	12.75	13.50	13.80
L1	-	3.10	3.40
	3.40	3.60	3.80
Q	2.60	2.80	3.00

Version 1: TO220-P package outline dimension

Ordering Information

Package Type	Units/ Tube	Tubes/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO220-P	50	20	1000	6	6000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG65R900PF	TO220	yes	yes	yes

