

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

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Parameter	Value	Unit
$V_{DS, \min} @ T_{j(\max)}$	750	V

I<sub>D</sub>,

**Absolute Maximum Ratings** at  $T_j=25$  unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	$V_{DS}$	700	V
Gate-source voltage	$V_{GS}$	$\pm 30$	V
Continuous drain current <sup>1)</sup> , $T_C=25\text{ }^\circ\text{C}$	$I_D$	4	A
Continuous drain current <sup>1)</sup> , $T_C=100\text{ }^\circ\text{C}$		2.5	
Pulsed drain current <sup>2)</sup> , $T_C=25\text{ }^\circ\text{C}$	$I_{D,\text{pulse}}$	12	A
Continuous diode forward current <sup>1)</sup> , $T_C=25\text{ }^\circ\text{C}$	$I_S$	4	A
Diode pulsed current <sup>2)</sup> , $T_C=25\text{ }^\circ\text{C}$	$I_{S,\text{pulse}}$	12	A
Power dissipation <sup>3)</sup> , $T_C=25\text{ }^\circ\text{C}$	$P_D$	28.4	W
Single pulsed avalanche energy <sup>5)</sup>	$E_{AS}$	85	mJ
MOSFET dv/dt ruggedness, $V_{DS}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}$	dv/dt	15	V/ns
Operation and storage temperature	$T_{stg}, T_j$	-55 to 150	$^\circ\text{C}$

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	R	4.4	$^\circ\text{C}/\text{W}$
Thermal resistance, junction-ambient <sup>4)</sup>	R	62	$^\circ\text{C}/\text{W}$

**Electrical Characteristics** at  $T_j=25$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	$BV_{DSS}$	700			V	$V_{GS}=0\text{ V}, I_D=250\text{ A}$
		750	810			$V_{GS}=0\text{ V}, I_D=2\text{ A}, T_j=150\text{ }^\circ\text{C}$
Gate threshold voltage	$V_{GS(\text{th})}$	2.0		4.0	V	$V_{DS}=V_{GS}, I_D=250\text{ A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		1.25	1.4		$V_{GS}=10\text{ V}, I_D=2\text{ A}$
			3.3			$V_{GS}=10\text{ V}, I_D=2\text{ A}, T_j=150\text{ }^\circ\text{C}$
Gate-source leakage current	$I_{GSS}$			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	$I_{DSS}$			1	A	$V_{DS}=700\text{ V}, V_{GS}=0\text{ V}$

## Dynamic Characteristics

Parameter	Symbol
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**Electrical Characteristics Diagrams**

<b>Figure 1. Typ. output characteristics</b>	<b>Figure 2. Typ. transfer characteristics</b>
<b>Figure 3. Typ. capacitances</b>	<b>Figure 4. Typ. gate charge</b>
<b>Figure 5. Drain-source breakdown voltage</b>	<b>Figure 6. Drain-source on-state resistance</b>

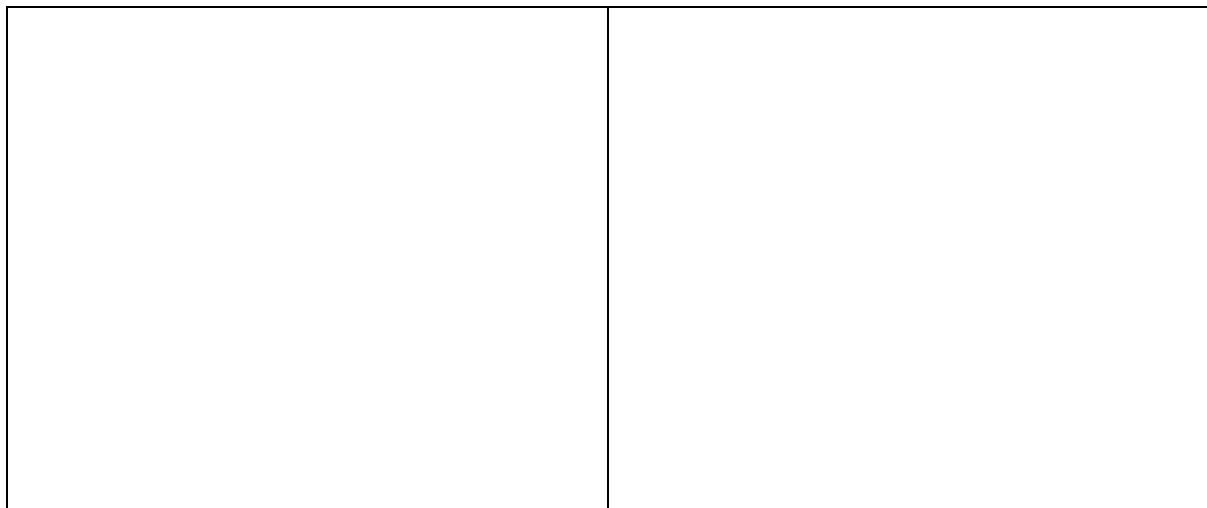
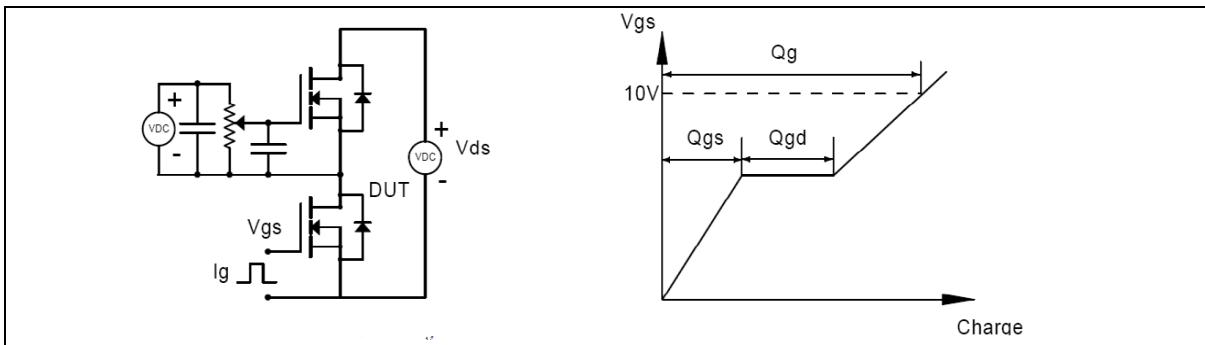


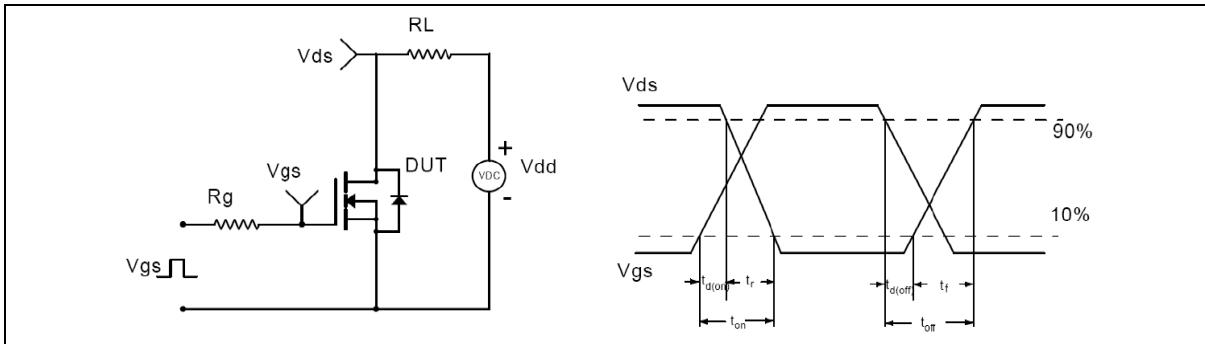
Figure 7. Forward characteristic of body diode

Figure 8. Drain-source on-state resistance

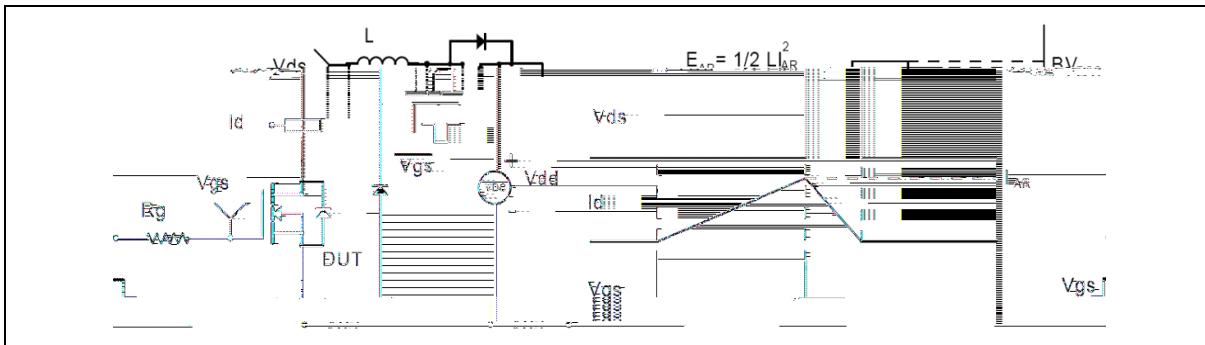
### Test circuits and waveforms



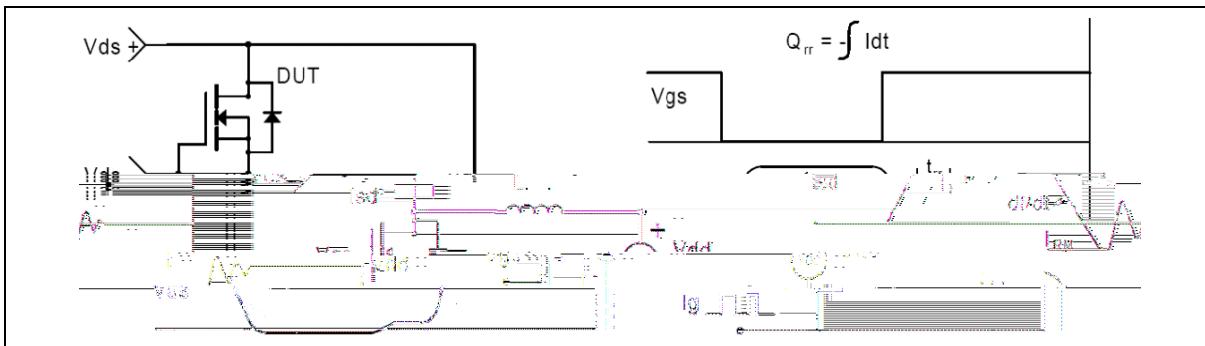
**Figure 1. Gate charge test circuit & waveform**



**Figure 2. Switching time test circuit & waveforms**

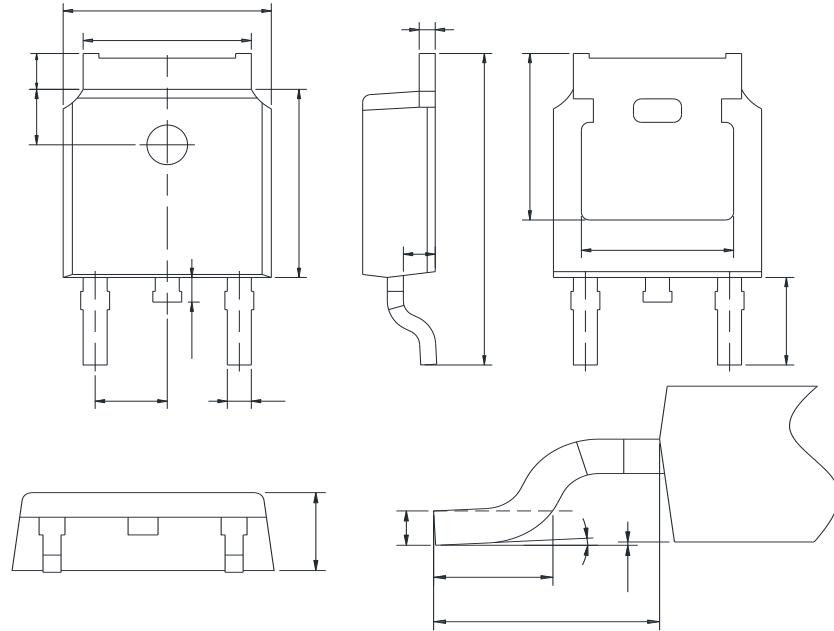


**Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms**



**Figure 4. Diode reverse recovery test circuit & waveforms**

## Package Information



Symbol	mm		
	Min	Nom	Max
A	2.20	2.30	2.38
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.73
E1	4.63	-	-
e	2.286BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	-	1.28
L4	0.50	-	1.00
	0	-	

Version 1: TO252-C package outline dimension



## Ordering Information

Package Type	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO252-C	2500	2	5000	5	25000

## Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG70R1K4DF	TO252	yes	yes	yes

