The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

GreenMOS®

RoHS

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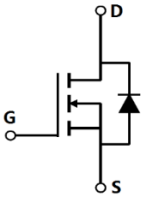
Parameter	Value	Unit
$V_{DS,\ min}$ @ $T_{j(max)}$	850	V
$I_{D,\ pulse}$	15	A
$R_{DS(ON)\ ,\ max}$ @ V_{GS}=10V	900	
Q_g	11.7	nC

Product Name	Package	Marking
OSG80R900FF	TO220F	OSG80R900F



Absolute Maximum Ratings at T_j=25 unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	800	V
Gate-source voltage	V_{GS}	±30	V
Continuous drain current[1], T_C=25 °C	I_D	5	A
Continuous drain current[1], T_C=100 °C		3.2	
Pulsed drain current[2], T			

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}				pF	V_{GS}=0 V, V_{DS}=50 V, 00 kHz
Output capacitance	C_{oss}		36.1		pF	
Reverse transfer capacitance	C_{rss}		1.5		pF	
Turn-on delay time	$t_{d(on)}$		31.3		ns	V_{GS}=10 V, V_{DS}=400 V, R_G I_D=4 A
Rise time	t_r		16.4		ns	
Turn-off delay time	$t_{d(off)}$		54.6		ns	
Fall time	t_f		7.0		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition

Electrical Characteristics Diagrams

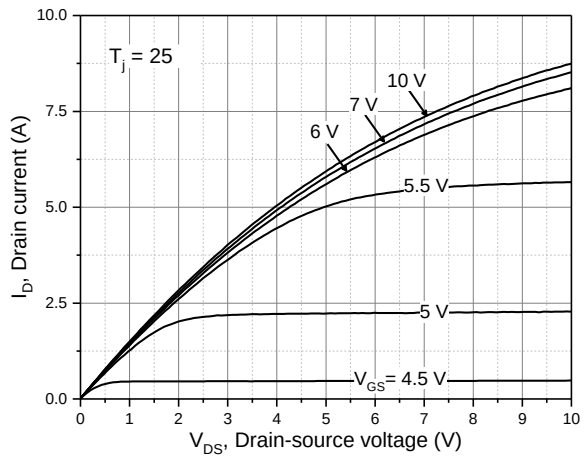


Figure 1. Typ. output characteristics

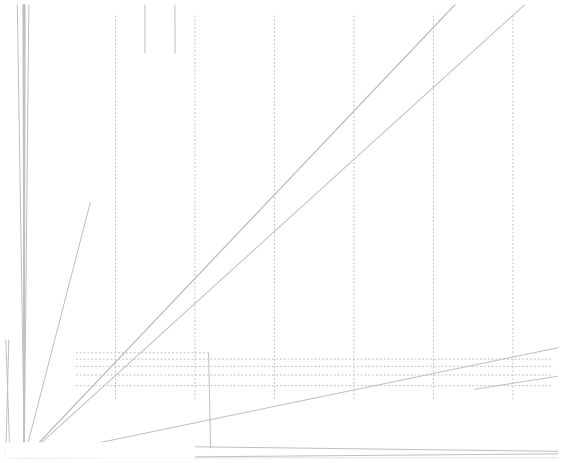


Figure 2. Typ. transfer characteristics

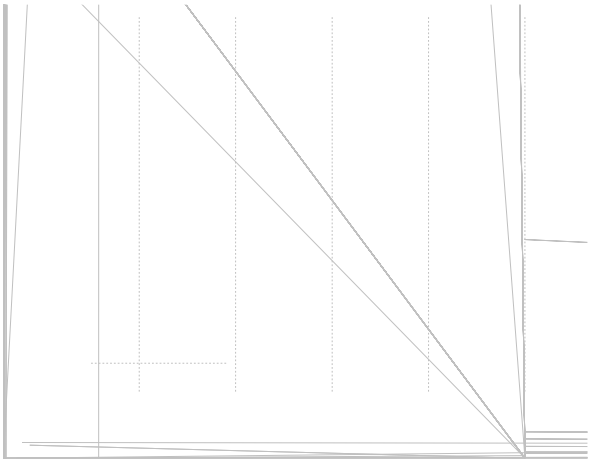


Figure 3. Typ. capacitances

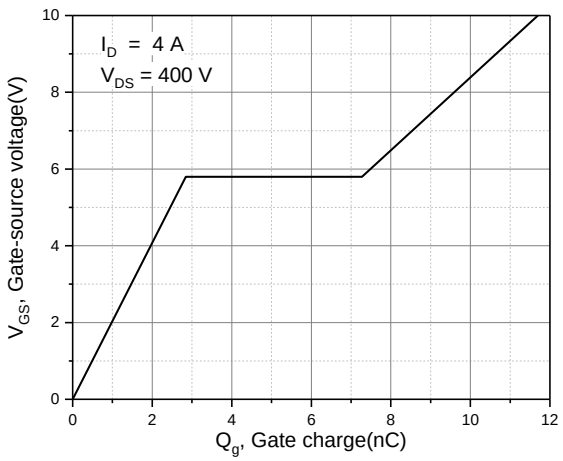


Figure 4. Typ. gate charge

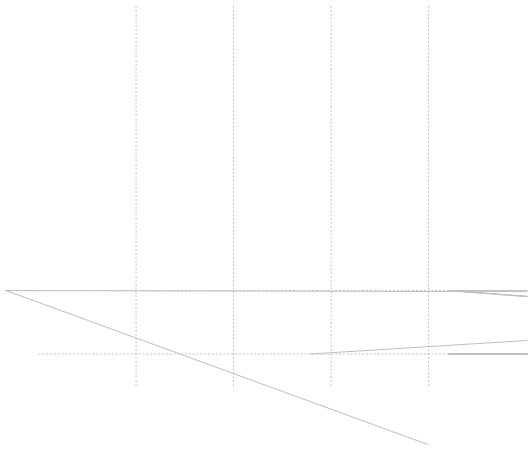


Figure 5. Drain-source breakdown voltage

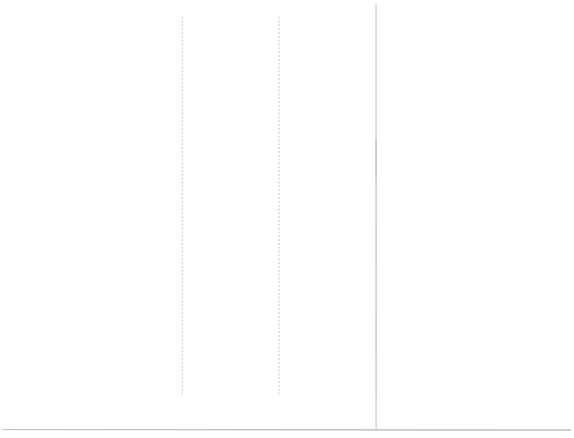


Figure 6. Drain-source on-state resistance

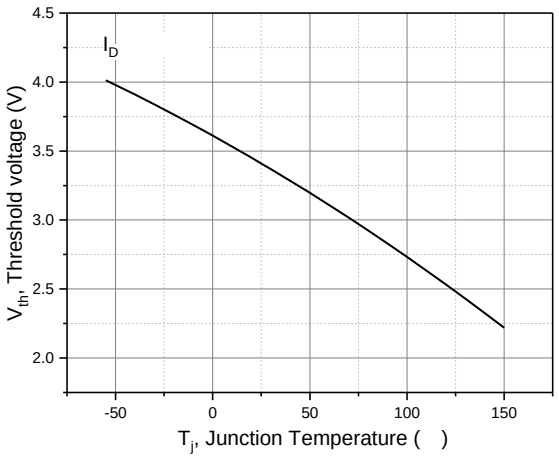


Figure 7. Threshold voltage

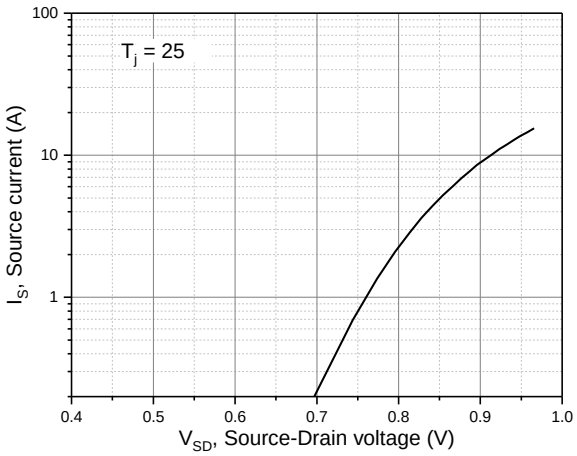


Figure 8. Forward characteristic of body diode

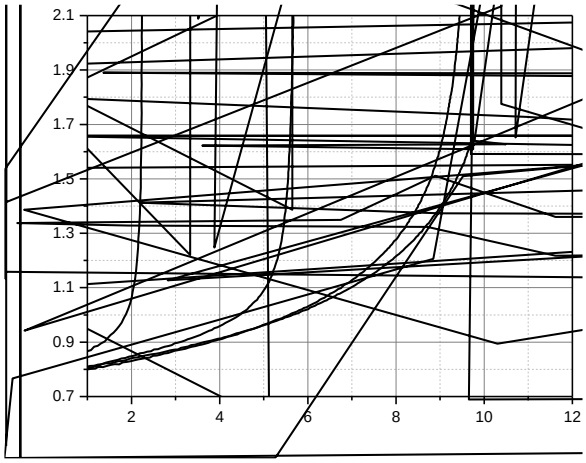


Figure 9. Drain-source on-state resistance

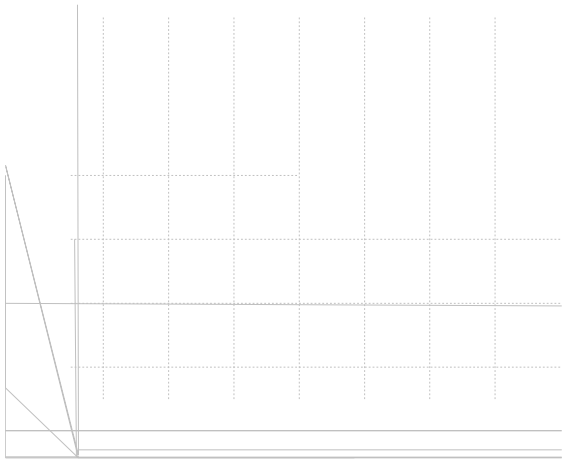


Figure 10. Drain current

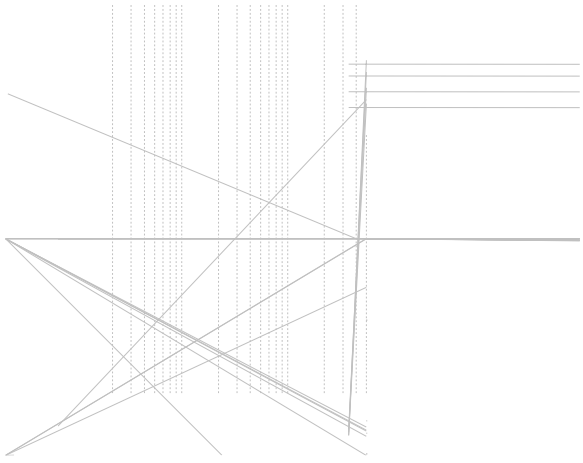


Figure 11. Safe operation area T_C=25 °C

Test circuits and waveforms

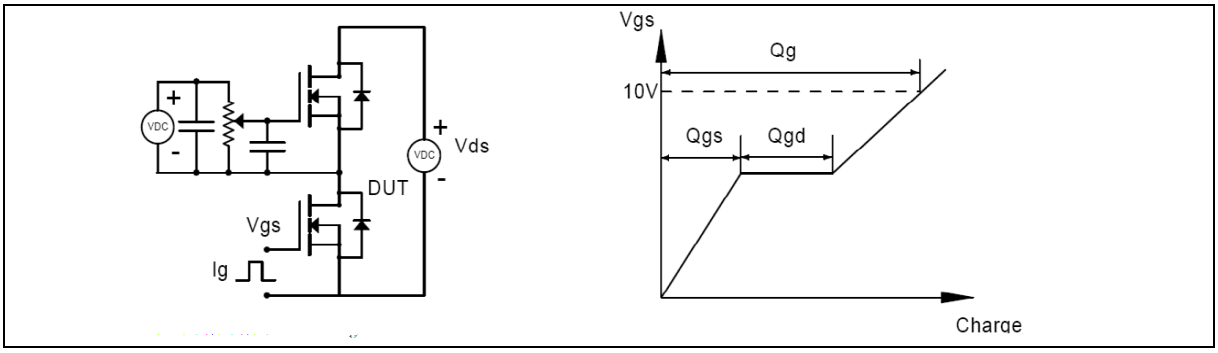


Figure 1. Gate charge test circuit & waveform

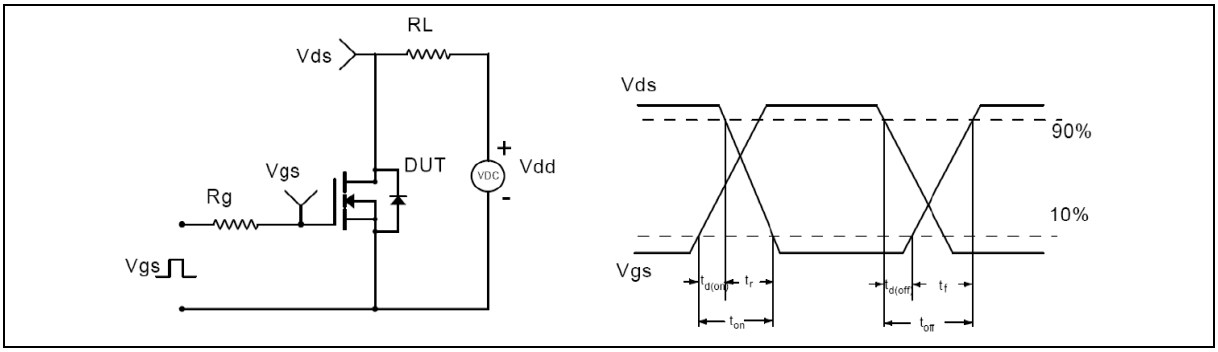


Figure 2. Switching time test circuit & waveforms

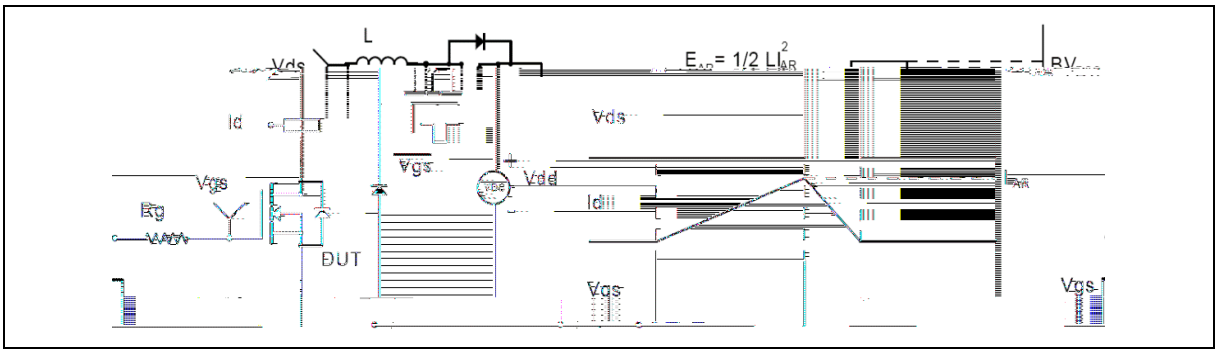


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

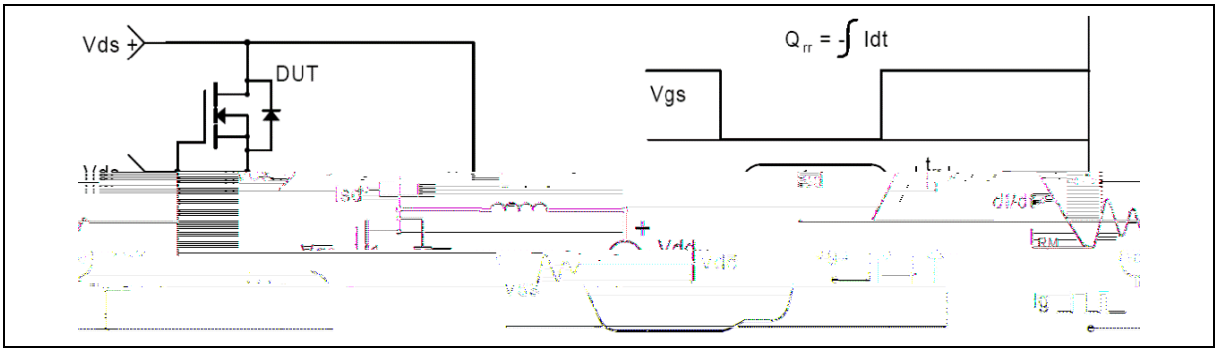


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information

mm		
Min	Nom	Max
9.96	10.16	10.36
4.50	4.70	4.90
2.34	2.54	2.74
2.56	2.76	2.96
0.40	0.50	0.65
15.57	15.87	16.17
	6.70REF	
	2.54BSC	
12.68	12.98	13.28

Ordering Information

Package Type	Units/ Tube	Tubes/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO220F-C	50	20	1000	6	6000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG80R900FF	TO220F	yes	yes	yes

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