

Features

- Low gate charge
- 100% avalanche tested
- Improved dv/dt capability
- RoHS compliant
- Halogen free package
- JEDEC Qualification
- Improved ESD performance

Absolute Maximum Ratings

Parameter	Symbol	TMP2N60AZ(G)	TMPF2N60AZ(G)	Unit	
Drain-Source Voltage	V_{DSS}	600		V	
Gate-Source Voltage	V_{GS}	30		V	
Continuous Drain Current	I_D	$T_C = 25$	2.0 *	A	
		$T_C = 100$	1.43 *	A	
Pulsed Drain Current ^(Note 1)	I_{DM}	8	8 *	A	
Single Pulse Avalanche Energy ^(Note 2)	E_{AS}	66		mJ	
Repetitive Avalanche Current ^(Note 1)	I_{AR}	2.0		A	
Repetitive Avalanche Energy ^(Note 1)	E_{AR}	5.21		mJ	
Power Dissipation	P_D	$T_C = 25$	52.1	17.3	W
		Derate above 25	0.416	0.138	W/
Peak Diode Recovery dv/dt ^(Note 3)	dv/dt	4.5		V/ns	
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150			
Maximum lead temperature for soldering purposes,					

Electrical Characteristics : $T_C=25$, unless otherwise noted

Parameter	Symbol	Test condition	Min	Typ	Max	Units
OFF						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	600	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 600\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	μA
		$V_{DS} = 480\text{ V}, T_C = 125^\circ\text{C}$	--	--	10	μA
Forward Gate-Source Leakage Current	I_{GSSF}	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	μA
Reverse Gate-Source Leakage Current	I_{GSSR}	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	μA

ON

Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	3	--	5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 1.0\text{ A}$	--	3.2	4.0	Ω
Forward Transconductance ^(Note 4)	g_{FS}	$V_{DS} = 30\text{ V}, I_D = 1.0\text{ A}$	--	3	--	S

DYNAMIC

Input Capacitance	C_{iss}	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	360	--	pF
Output Capacitance	C_{oss}		--	41	--	pF
Reverse Transfer Capacitance	C_{rss}		--	7	--	pF

SWITCHING

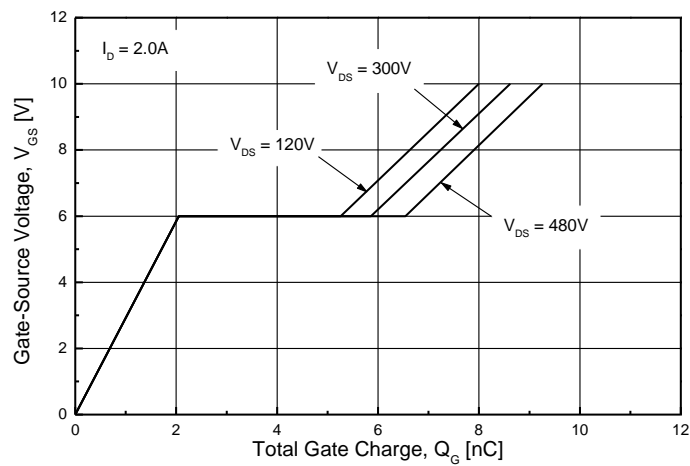
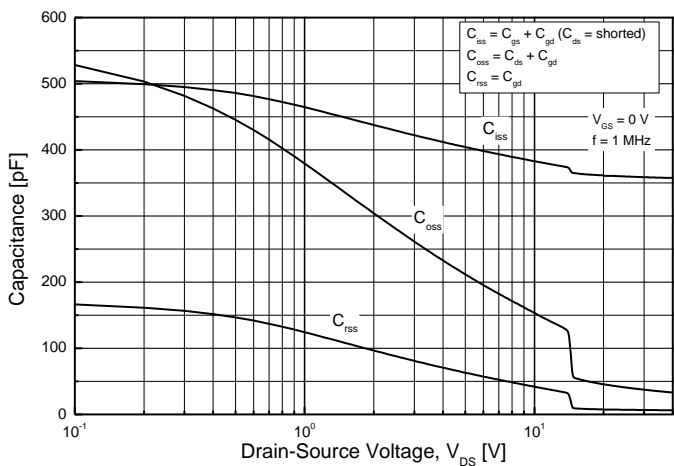
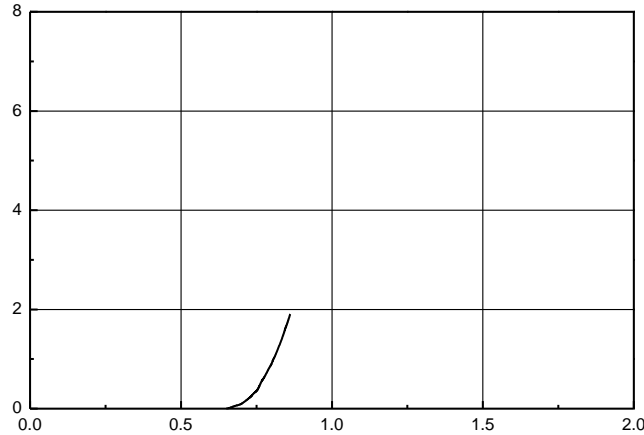
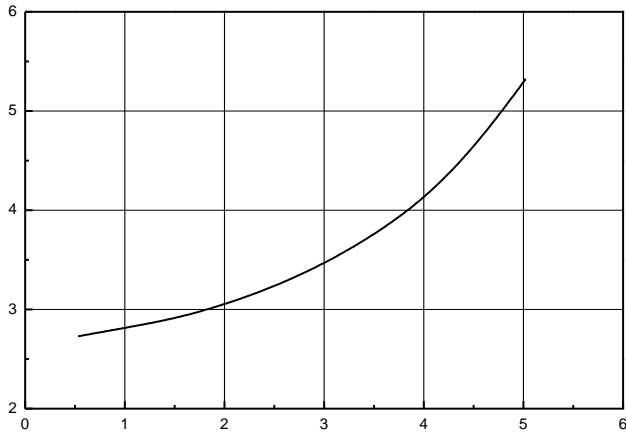
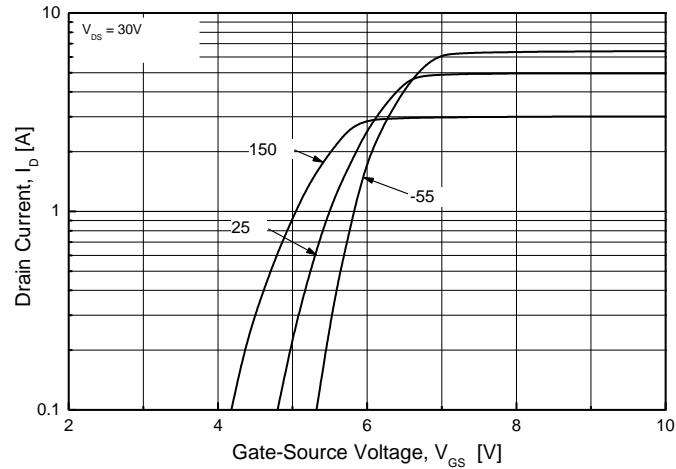
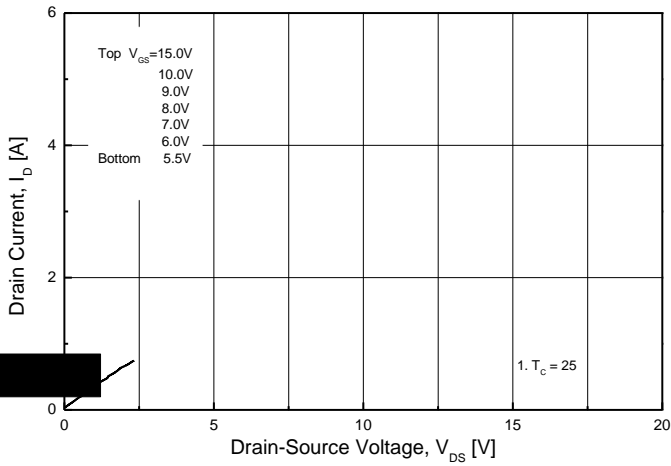
Turn-On Delay Time ^(Note 4,5)	$t_{d(on)}$	$V_{DD} = 300\text{ V}, I_D = 2.0\text{ A},$ $R_G = 25\ \Omega, V_{GS} = 10\text{ V}$	--	20	--	ns
Turn-On Rise Time ^(Note 4,5)	t_r		--	23	--	ns
Turn-Off Delay Time ^(Note 4,5)	$t_{d(off)}$		--	42	--	ns
Turn-Off Fall Time ^(Note 4,5)	t_f		--	20	--	ns
Total Gate Charge ^(Note 4,5)	Q_g	$V_{DS} = 480\text{ V}, I_D = 2.0\text{ A},$ $V_{GS} = 10\text{ V}$	--	9	--	nC
Gate-Source Charge ^(Note 4,5)	Q_{gs}		--	2	--	nC
Gate-Drain Charge ^(Note 4,5)	Q_{gd}		--	4	--	nC

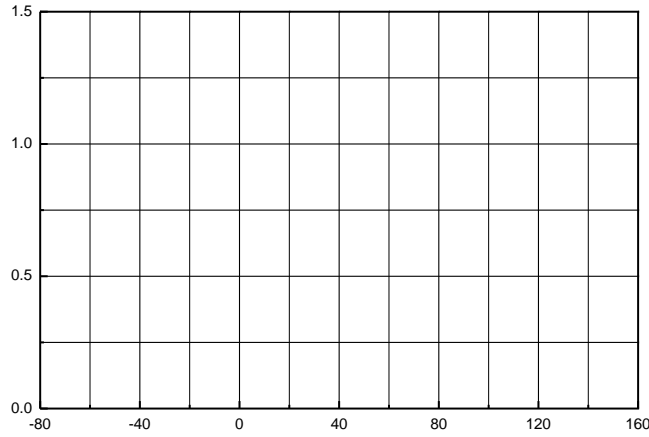
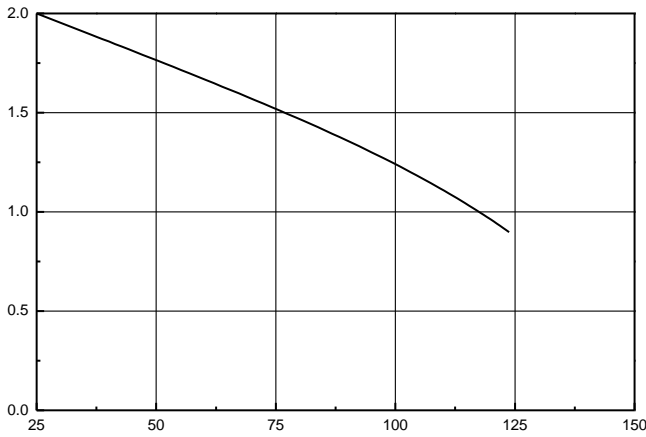
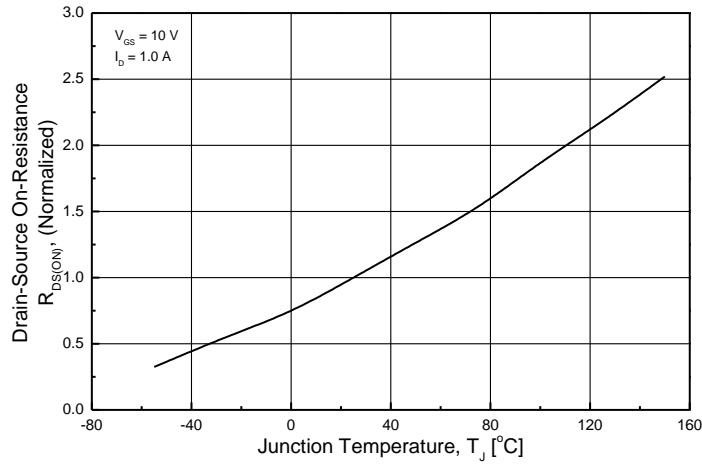
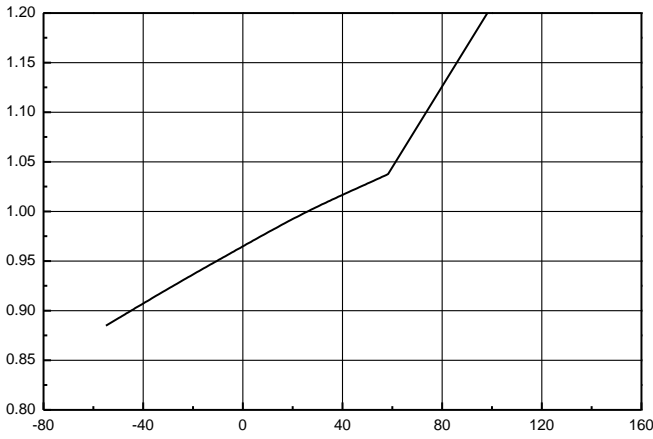
SOURCE DRAIN DIODE

Maximum Continuous Drain-Source Diode Forward Current	I_S	----	--	--	2.0	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}	----	--	--	8	A
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{ V}, I_S = 2.0\text{ A}$	--	--	1.5	V
Reverse Recovery Time ^(Note 4)	t_{rr}	$V_{GS} = 0\text{ V}, I_S = 2.0\text{ A}$ $di_F / dt = 100\text{ A}/\mu\text{s}$	--	240	--	ns
Reverse Recovery Charge ^(Note 4)	Q_{rr}		--	0.8	--	μC

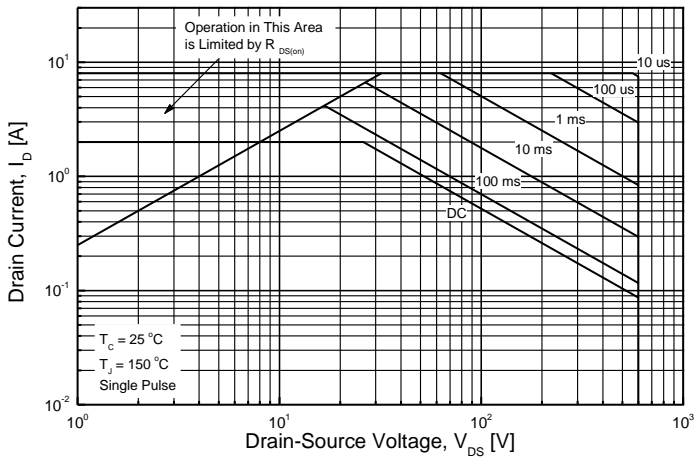
Note :

1. Repeated rating : Pulse width limited by safe operating area
2. $L=30.5\text{mH}, I_{AS} = 2.0\text{A}, V_{DD} = 50\text{V}, R_G = 25\ \Omega$, Starting $T_J = 25$
3. $I_{SD} = 2.0\text{A}, di/dt = 100\ \mu\text{A}/\mu\text{s}, V_{DD} = 50\text{V}, V_{DS} = 480\text{V}$, Starting $T_J = 25$
5. Essentially Independent of Operating Temperature Typical Characteristics

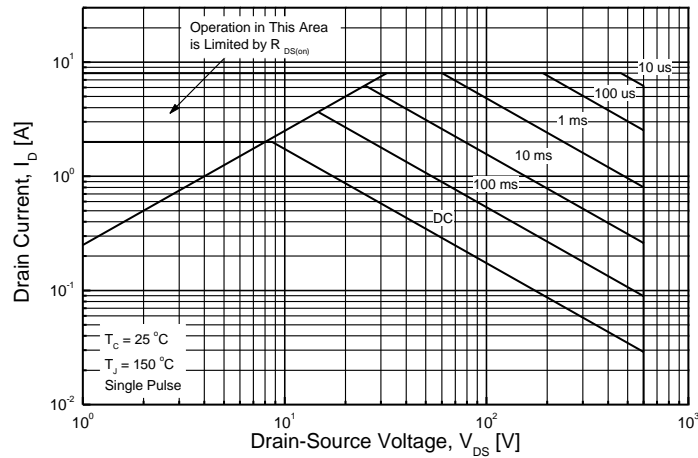




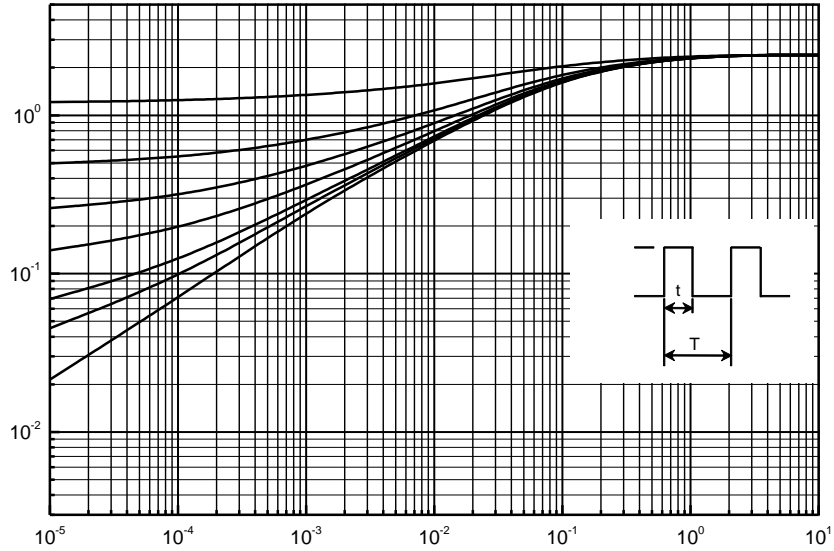
TMP2N60AZ(G)



TMPF2N60AZ(G)

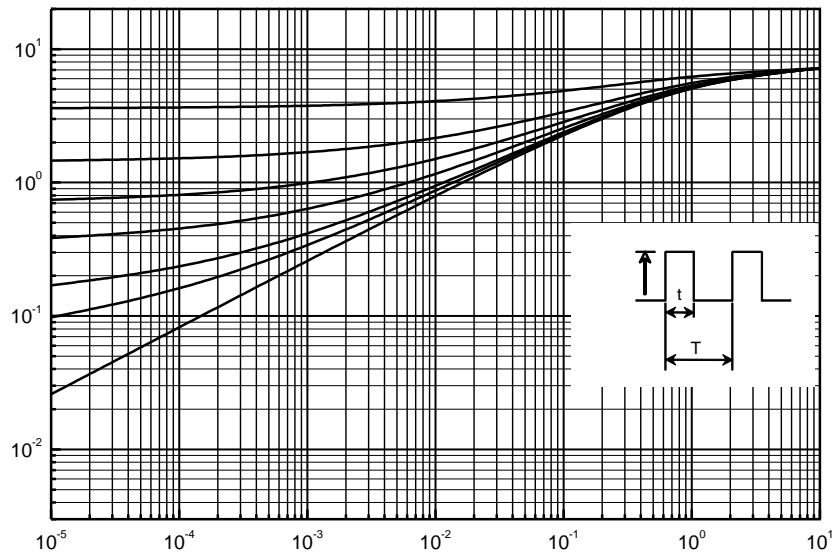


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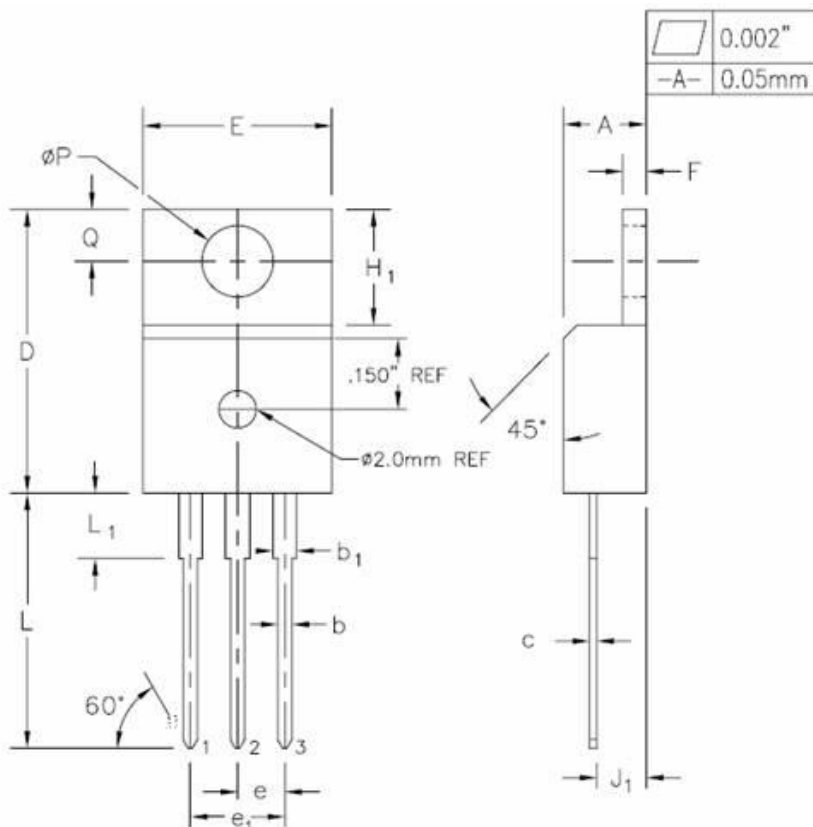


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TMPF2N60AZ(G)



TO-220AB-3L MECHANICAL DATA



SYMBOL	INCHES		MILLIMETERS		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	0.170	0.180	4.32	4.57	
F	0.028	0.036	0.71	0.91	
b ₁	0.045	0.055	1.15	1.39	
c	0.014	0.021	0.36	0.53	
D	0.590	0.610	14.99	15.49	
E	0.395	0.410	10.04	10.41	
e	0.100 TYP.		2.54 TYP.		
e ₁	0.200 BSC		5.08 BSC		
F	0.048	0.054	1.22	1.37	
H ₁	0.235	0.255	5.97	6.47	
J ₁	0.100	0.110	2.54	2.79	
L	0.530	0.550	13.47	13.97	
L ₁	0.130	0.150	3.31	3.81	2
φP	0.149	0.153	3.78	3.90	
Q	0.102	0.112	2.60	2.84	

