Ω

$R_{DS(on),typ}$ Ω

=25 (unless otherwise specified)

=25
=100
=25

Power Dissipation	=25	W
Operating and Storage Temperature		

θ_{JC}	/W
θ_{JA}	/W

=25 (unless otherwise specified)

				typ		
		μ				
		μ				
		=25				μ
		=100				
						Ω
	fs					
		Open, f=1MHz				Ω

Input Capacitance		=75V, f=1MHz				pF
Output Capacitance						
Reverse Transfer Capacitance						
		Ω				
Turn off Delay Time	d(off)					
	f					

		/dt=100A/μ				

Fig 1. Typical Output Characteristics	

	Figure 4. Normalized On-Resistance vs. Junction Temperature

Figure 5. Typical Transfer Characteristics	Figure 6. Typical Source-Drain Diode Forward Voltage

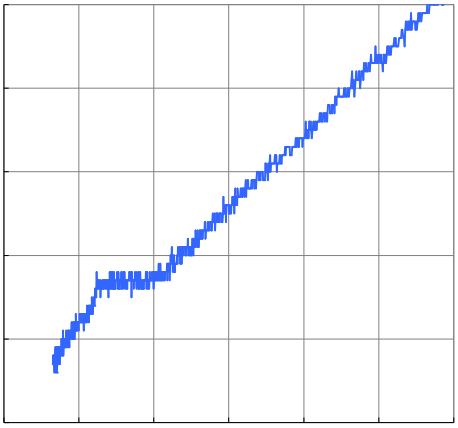
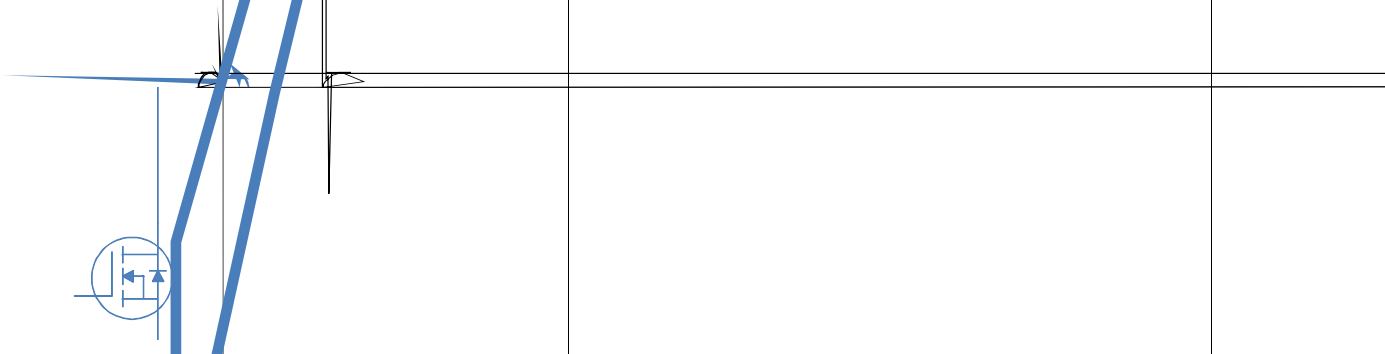
Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage	Figure 8. Typical Capacitance vs. Drain-to-Source Voltage
	

Figure 9. Maximum Safe Operating Area	Figure 10. Maximun Drain Current vs. Case Temperature

Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Case

Hunteck

Uclamped Inductive Switching (UIS) Test



Dimentions in mm unless otherwise specified

Dimentions in mm unless otherwise specified
