100V N-Ch Power MOSFET

Feature

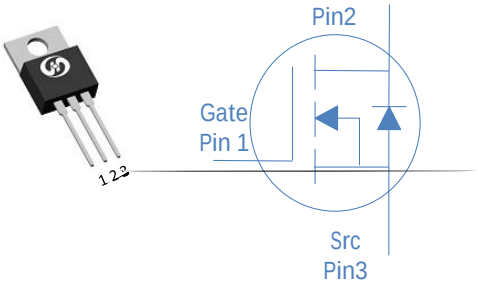
- High Speed Power Switching
- Enhanced Body diode dv/dt capability
- Enhanced Avalanche Ruggedness
- 100% UIS Tested, 100% Rg Tested
- Lead Free

Application

- Synchronous Rectification in SMPS
- Hard Switching and High Speed Circuit
- Power Tools
- UPS
- Motor Control

V_{DS}		100	V
$R_{DS(on),typ}$	TO-263	2.8	mΩ
$R_{DS(on),typ}$	TO-247	3	mΩ
$R_{DS(on),typ}$	TO-220	3.1	mΩ
$I_{D\ (Sillicon\ Limited)}$		190	A
$I_{D\ (Package\ Limited)}$		120	A

TO-263 TO-220



Part Number	Package	Marking
HGB037N10S	TO-263	GB037N10S
HGK037N10S	TO-247	GK037N10S
HGP037N10S	TO-220	GP037N10S

Absolute Maximum Ratings at T_j

Parameter	Symbol	Conditions	Value	Unit
Continuous Drain Current (Silicon Limited)	I_D	T_C	190	A
		T_C	134	
Continuous Drain Current (Package Limited)		T_C	120	
Drain to Source Voltage	V_{DS}	-	100	V
Gate to Source Voltage	V_{GS}	-	±20	V
Pulsed Drain Current	I_{DM}	-	400	A
Avalanche Energy, Single Pulse	E_{AS}	L=0.1mH, T_C	180	mJ
Power Dissipation	P_D	T_C	300	
Operating and Storage Temperature	T_J, T_{stg}	-	-55 to175	

Absolute Maximum Ratings

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-Case	$R_{\theta JC}$	0.5	
Thermal Resistance Junction-Ambient	$R_{\theta JA}$	60	

Electrical Characteristics at T_j

Static Characteristics

Parameter	Symbol	Conditions		Value			Unit
				min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V, I_D=250μA		100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{GS}=V_{DS}, I_D=250μA		2	2.8	4	
Zero Gate Voltage Drain Current	I_{DSS}	V_{GS}=0V, V_{DS}=100V, T_j		-	-	1	μA
		V_{GS}=0V, V_{DS}=100V, T_j		-	-	100	
Gate to Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V		-	-	±100	nA
Drain to Source on Resistance	$R_{DS(on)}$	V_{GS}=10V, I_D=20A	TO-263	-	2.8	3.4	mΩ
			TO-247	-	3	3.6	
			TO-220	-	3.1	3.7	
Transconductance	g	V_{DS}=5V, I_D=20A		-	90	-	S
Gate Resistance	R_G	V_{GS}=0V, V_{DS}		-	0.7	-	Ω

Dynamic Characteristics

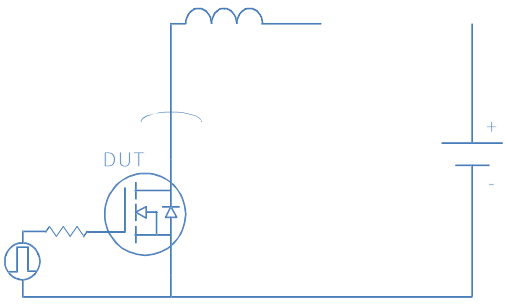
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}	-	7300	-	pF
Output Capacitance	C_{oss}		-	580	-	
	C_{rss}		-	18	-	
Total Gate Charge	Q_g	V_{DD}=50V, I_D=20A, V_{GS}=10V	-	118	-	nC
Gate to Source Charge	Q_{gs}		-	27	-	
Gate to Drain (Miller) Charge	Q_{gd}		-	21	-	
Turn on Delay Time	$t_{d(on)}$	V_{DD}=50V, I_D=20A, V_{GS}=10V, R_G=10Ω,	-	35	-	ns
Rise time	t_r		-	56	-	
	t		-	92	-	
Fall Time	t		-	26	-	

Reverse Diode Characteristics

Diode Forward Voltage	V_{SD}	V_{GS}=0V, I_F=20A	-	0.9	1.2	V
Reverse Recovery Time	t_{rr}	V_R=50V, I_F=30A, dI_F / μs	-	64	-	ns
Reverse Recovery Charge	Q_{rr}		-	550	-	nC

Inductive switching Test	

Gate Charge Test	

Uclamped Inductive Switching (UIS) Test	
	

Diode Recovery Test	

Package Outline

TO-220, 3 leads

Symbol	Min	Nom	Max
A	9.66	9.97	10.28
A2	9.80	10.00	10.20
B	15.60	15.70	15.80
	12.70	13.48	14.27
	4.30	4.50	4.70
	9.00	9.20	9.40
F		2.54	
G1	1.32	1.52	1.72
G2	0.70	0.82	0.95
G3	0.45	0.52	0.60
H	3.50	3.60	3.70
I	2.70	2.80	2.90
J	15.70	15.97	16.25
K	2.20	2.40	2.60
L	1.15	1.27	1.40
N	6.40	6.60	6.80

TO-263, 2 leads

Symbol	Min	Nom	Max
A	9.66	9.97	10.28
B	1.02	1.17	1.32
C	8.59	9.00	9.40
D1	1.14	1.27	1.40
D2	0.70	0.83	0.95
D3		5.08	
E	15.09	15.24	15.39
F	1.15	1.28	1.40
G	4.30	4.50	4.70
H	2.29	2.54	2.79
I		0.25	
K	1.30	1.45	1.60
a1	0.45	0.55	0.65
a2(degree)	0°		8°