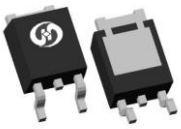
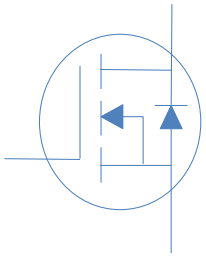
60V N-Ch Power MOSFET

V_{DS}	60	V
$R_{DS(on),typ}$	4.5	m
$I_{D\ (Sillicon\ Limited)}$	112	A
$I_{D\ (Package\ Limited)}$	70	A





Part Number	Package	Marking
HGD053N06S	TO-252	GD053N06S

Absolute Maximum Ratings at T_J=25℃ (unless otherwise specified)

Parameter	Symbol	Conditions	Value	Unit
Continuous Drain Current (Silicon Limited)	I_D	T_C=25℃	112	A
		T_C=100℃	79	
Continuous Drain Current (Package Limited)		T_C=25℃	70	
Drain to Source Voltage	V_{DS}	-	60	V
Gate to Source Voltage	V_{GS}	-	±20	V
Pulsed Drain Current	I_{DM}	-	250	A
Avalanche Energy, Single Pulse	E_{AS}	L=0.4mH, T_C=25℃	80	mJ
Power Dissipation	P_D	T_C=25℃	125	W
Operating and Storage Temperature	T_J, T_{stg}	-	-55 to175	℃

Absolute Maximum Ratings

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-Case	R_{JC}	1.2	℃/W
Thermal Resistance Junction-Ambient	R_{JA}	50	℃/W

Electrical Characteristics at T_j=25℃ (unless otherwise specified)

Static Characteristics

Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V, I_D=250 A	60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{GS}=V_{DS}, I_D=250 A	2	2.9	4	
Zero Gate Voltage Drain Current	I_{DSS}	V_{GS}=0V, V_{DS}=60V, T_j=25℃	-	-	1	A
		V_{GS}=0V, V_{DS}=60V, T_j=100℃	-	-	100	
Gate to Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
Drain to Source on Resistance	$R_{DS(on)}$	V_{GS}=10V, I_D=20A	-	4.5	5.3	m
Transconductance	g_{fs}	V_{DS}=5V, I_D=20A	-	60	-	S
Gate Resistance	R_G	V_{GS}=0V, V_{DS} Open, f=1MHz	-	1.9	-	

Dynamic Characteristics

Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=30V, f=1MHz	-	2207	-	pF
Output Capacitance	C_{oss}		-	660	-	
Reverse Transfer Capacitance	C_{rss}		-	24	-	
Total Gate Charge (10V)	Q_g (10V)	V_{DD}=30V, I_D=20A, V_{GS}=10V	-	35	-	nC
Gate to Source Charge	Q_{gs}		-	11	-	
Gate to Drain (Miller) Charge	Q_{gd}		-	7	-	
Turn on Delay Time	$t_{d(on)}$	V_{DD}=30V, I_D=20A, V_{GS}=10V, R_G=10 ,	-	11	-	ns
Rise time	t_r		-	7	-	
Turn off Delay Time	$t_{d(off)}$		-	34	-	
Fall Time	t_f		-	8	-	

Reverse Diode Characteristics

Diode Forward Voltage	V_{SD}	V_{GS}=0V, I_F=20A	-	0.9	1.2	V
Reverse Recovery Time	t_{rr}	V_R=30V, I_F=20A, dI_F/dt=300A/ s	-	30	-	ns
Reverse Recovery Charge	Q_{rr}		-	68	-	nC

Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage

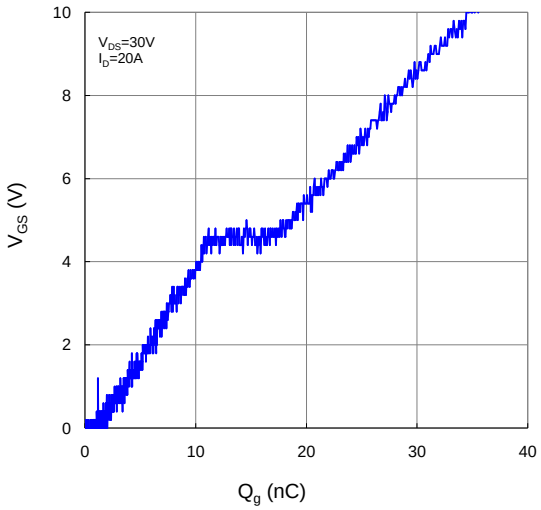


Figure 8. Typical Capacitance vs. Drain-to-Source Voltage

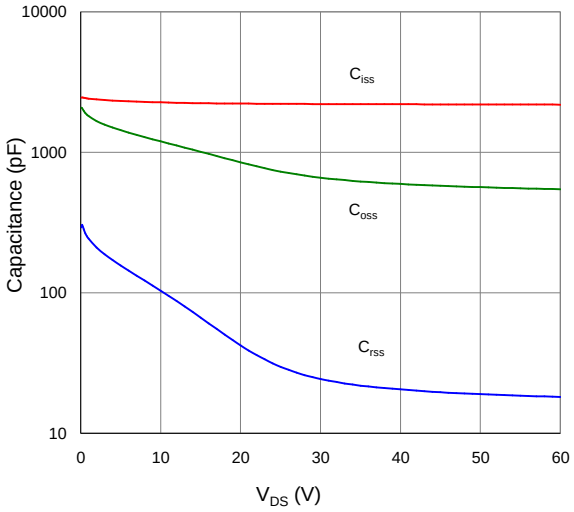


Figure 9. Maximum Safe Operating Area

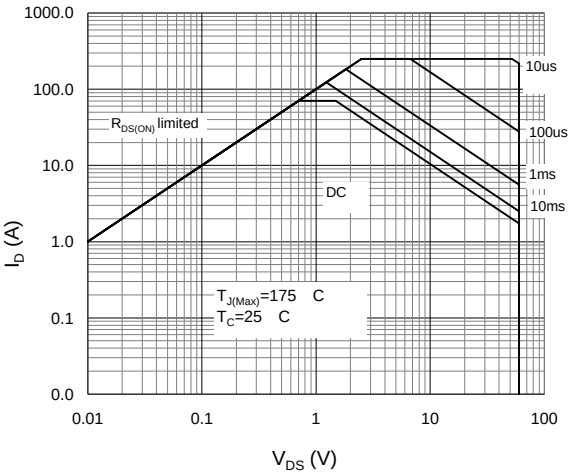


Figure 10. Maximun Drain Current vs. Case Temperature

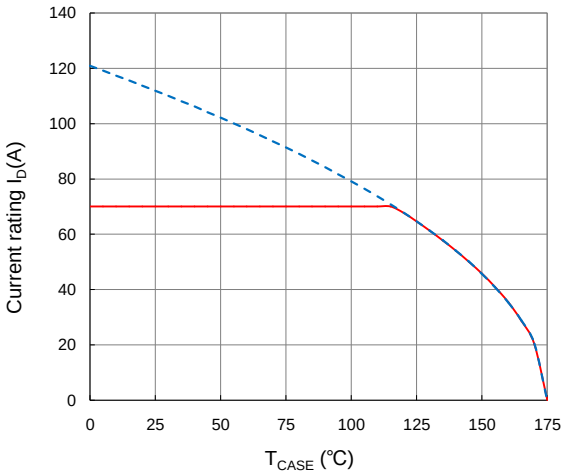
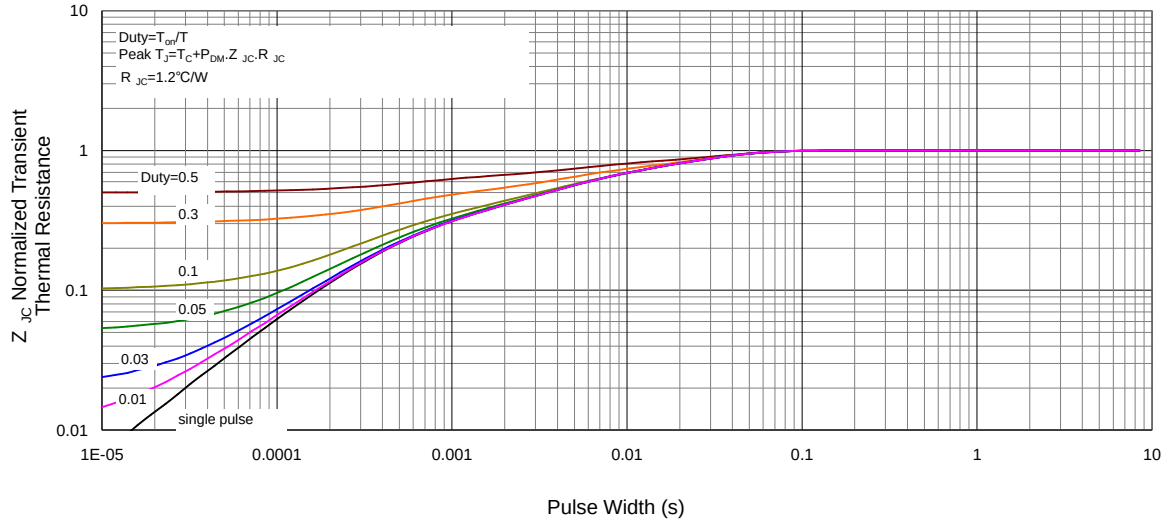
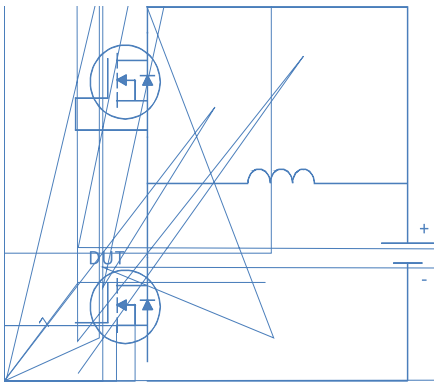
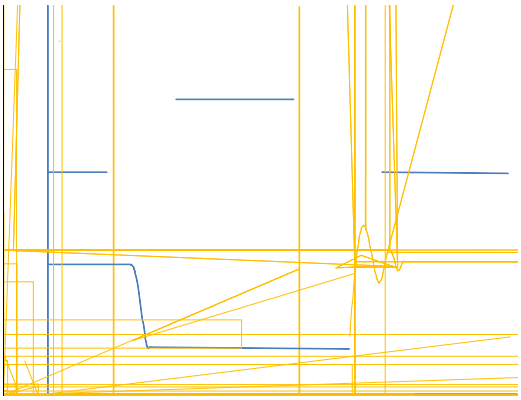


Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Case

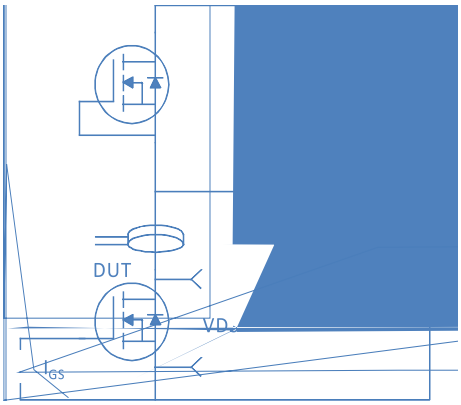


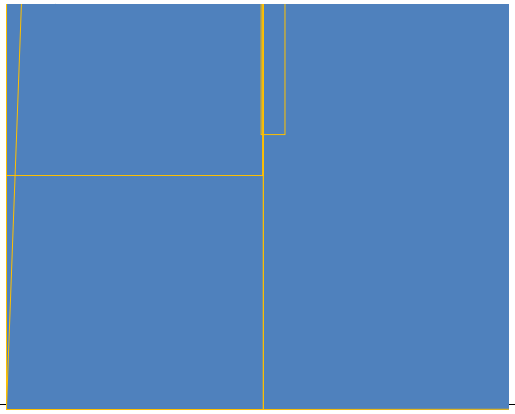
Inductive switching Test



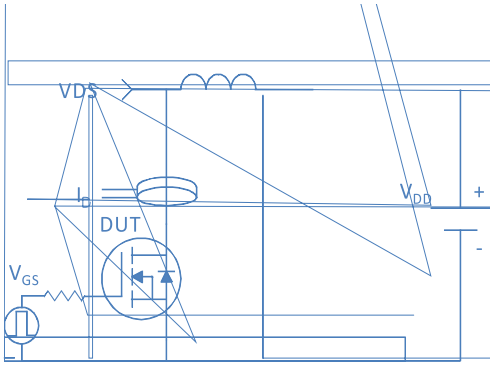


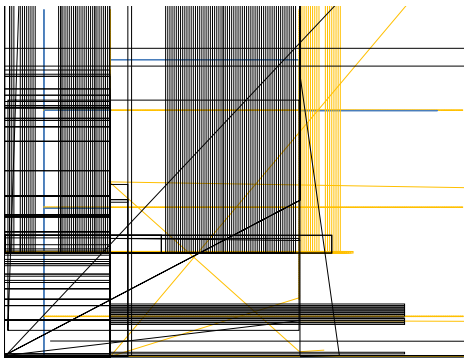
Gate Charge Test



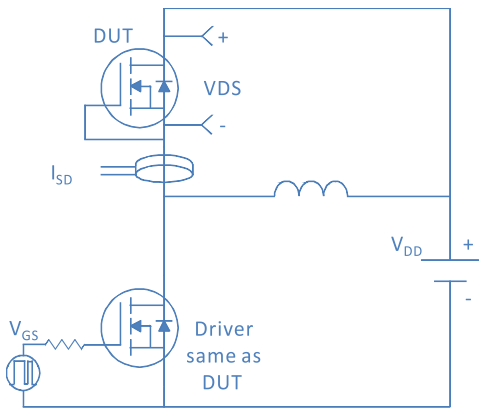


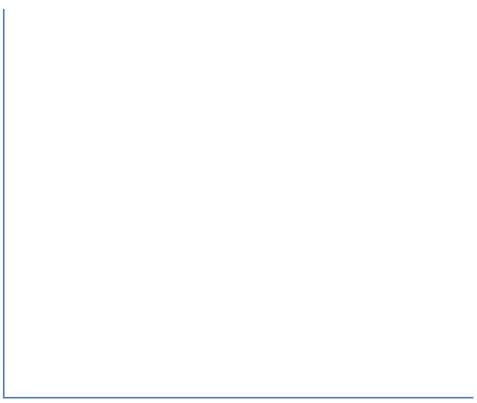
Uclamped Inductive Switching (UIS) Test





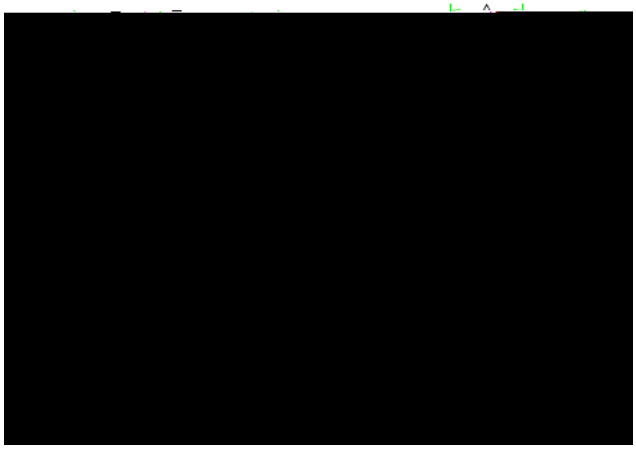
Diode Recovery Test



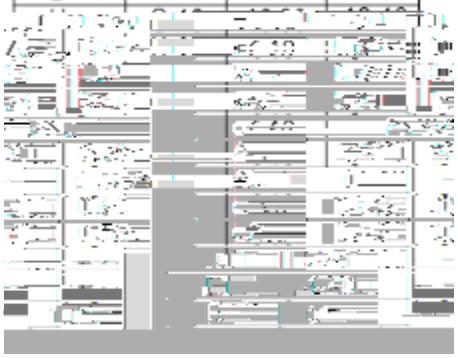


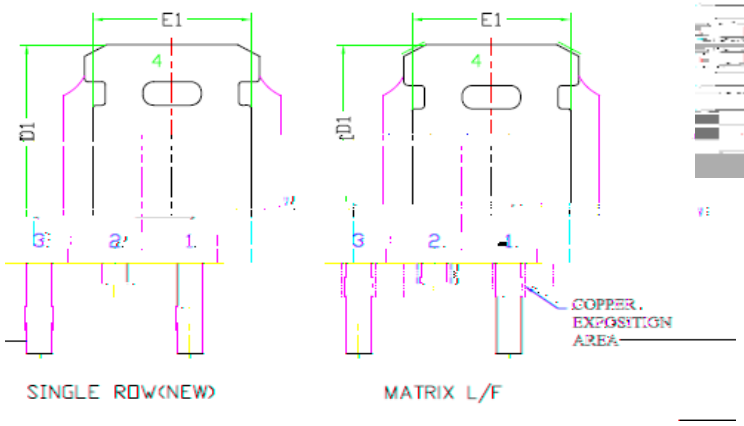
Package Outline

TO-252, 3 leads



SYMBOL	DIMENSIONAL REQMTS		
	MIN	NOM	MAX
E	6.40	6.60	6.731
L	1.40	1.52	1.77
L1		2.743 REF	
L2		0.508 BSC	
L3	0.89	--	1.27
L4	0.64	--	1.01
L5	--	--	--
D	6.00	6.10	6.223





SINGLE ROW(NEW) MATRIX L/F