Hunteck

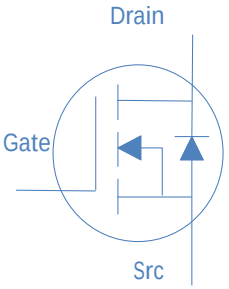
Feature

- High Speed Power Switching, Logic Level
- Enhanced Body diode dv/dt capability
- Enhanced Avalanche Ruggedness
- 100% UIS Tested, 100% Rg Tested
- Lead Free, Halogen Free

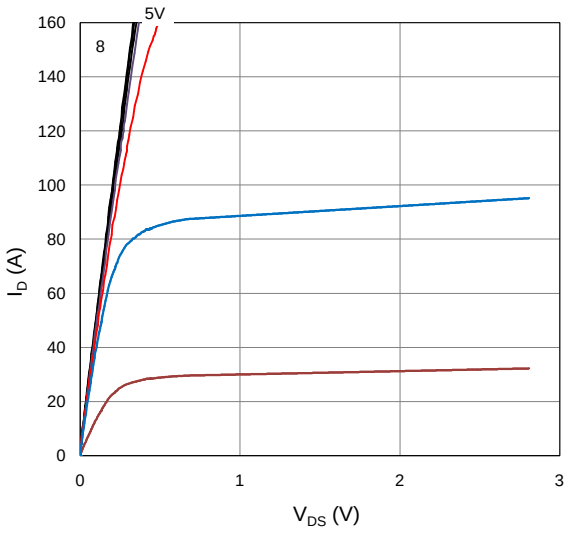
			Ω
			Ω

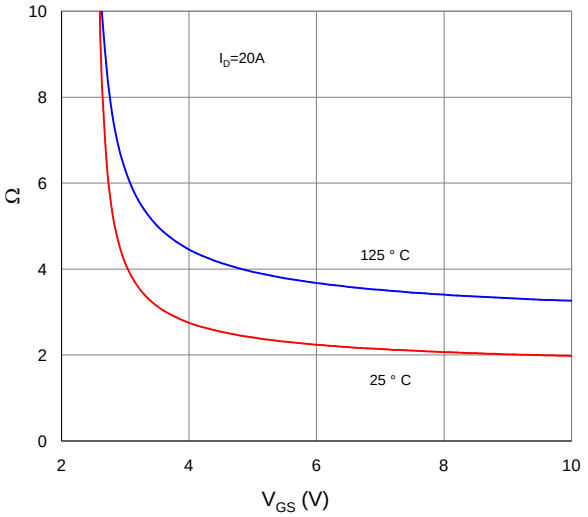
Application

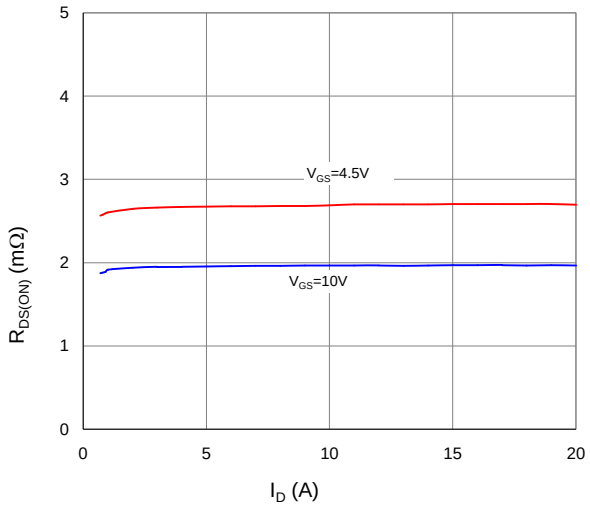
- Synchronous Rectification in SMPS
- Hard Switching and High Speed Circuit
- DC/DC in Telecoms and Inductrial

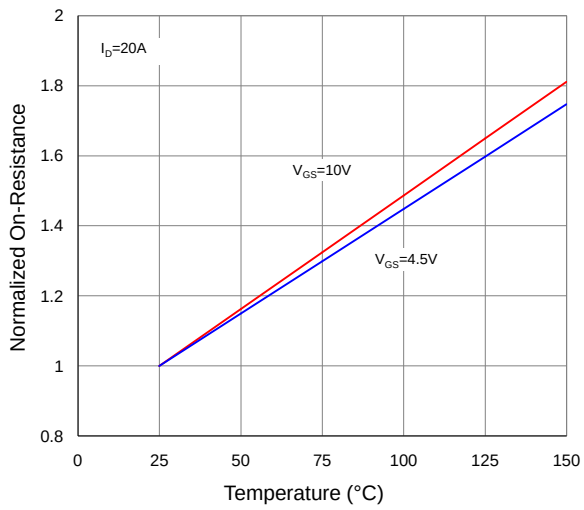


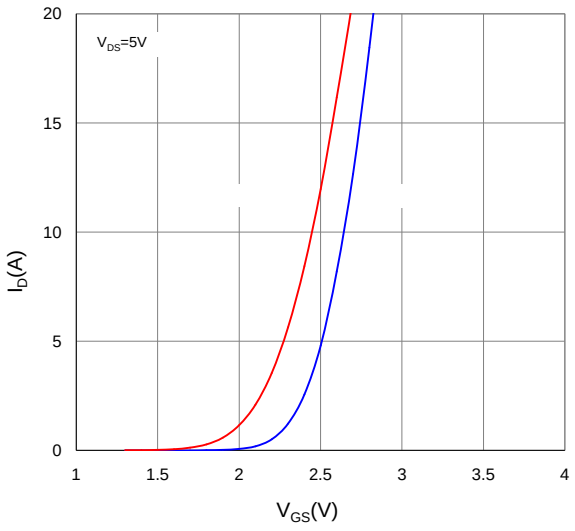
	θ		
	θ		

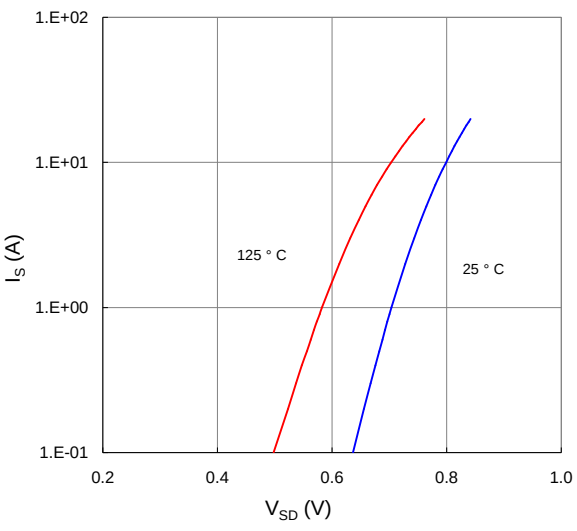
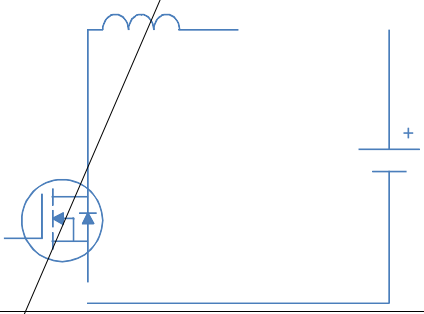

5V
8
160
140
120
100
80
60
40
20
0
0
1
2
3
I_D (A)
V_{DS} (V)


I_D=20A
125 ° C
25 ° C
10
8
6
4
2
0
2
4
6
8
10
Ω
V_{GS} (V)


V_{GS}=4.5V
V_{GS}=10V
5
4
3
2
1
0
0
5
10
15
20
$R_{DS(ON)}$ (mΩ)
I_D (A)


I_D=20A
V_{GS}=10V
V_{GS}=4.5V
2
1.8
1.6
1.4
1.2
1
0.8
0
25
50
75
100
125
150
Normalized On-Resistance
Temperature (°C)


V_{DS}=5V
20
15
10
5
0
1
1.5
2
2.5
3
3.5
4
I_D(A)
V_{GS}(V)


125 ° C
25 ° C
1.E+02
1.E+01
1.E+00
1.E-01
0.2
0.4
0.6
0.8
1.0
I_S (A)
V_{SD} (V)