



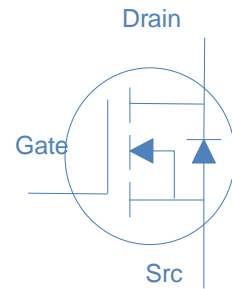
Feature

- High Speed Power Switching
- Enhanced Body diode dv/dt capability
- Enhanced Avalanche Ruggedness
- 100% UIS Tested 100% Rg Tested
- Lead Free, Halogen Free

			Ω
			Ω

Application

- Synchronous Rectification in SMPS
- Hard Switching and High Speed Circuit
- DC/DCn Telecoms and Industrial



=25 (unless otherwise specified)

		=25	
		=100	
		=25	
		=25	
		=25	

			μW
		θ	μW
		θ	μW

=25 (unless otherwise specified)

		μ				
		μ				
		=25				μ
		=100				
						Ω
						Ω
	fs					
		Open, f=1MHz				Ω

		=40V, f=1MHz				
Reverse Transfer Capacitance						
Turn off Delay Time	d(off)	Ω				
	f					

		$/dt=400A/\mu$				



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		Figure 4. Normalized On-Resistance vs. Junction Temperature

Figure 5. Typical Transfer Characteristics		



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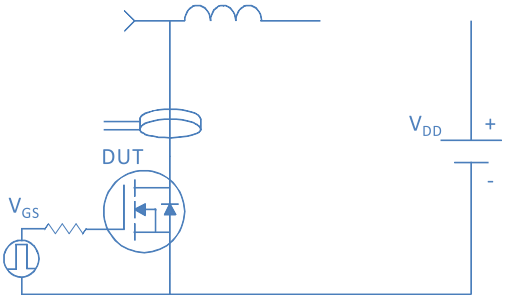
Figure 9. Maximum Safe Operating Area	
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Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Ambient
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Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A3	0.254 REF		0.010REF	
D	4.680	5.120	0.184	0.202
E	5.000	5.426	0.220	0.214
1.610	4.110	0.142	0.162	D1
1.380	3.780	0.133	0.149	D1
1.800	5.000	0.189	0.197	D2
1.674	5.826	0.223	0.229	D2
1.100	1.390	0.043	0.055	k
1.330	0.510	0.013	0.020	b
	1.2/0TYP		1.2/0TYP	e
1.510	0.711	0.020	0.028	L
0.017	0.023	L1	0.424	0.5/6
0.410	0.726	0.016	0.029	H
12°	θ	0°	12°	0°